# A Logic for Bytecode and the Translation of Proofs from Sequential Java 

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#### Abstract

The goal of this semester-thesis is twofold. Firstly, we are defining an operational semantics and a Hoare-style programming logic for an imaginary sequential, stack-based, bytecode language with local variables, objects and dynamic dispatch that resembles the JVM or the CLR. Secondly, we explain how to translate source-level proofs to the bytecode logic we have introduced in the first part.


## 1 Introduction

This paper defines a Hoare programming logic for a simple, sequential, stack based bytecode language with objects and dynamic dispatch. For the purpose of our explanations, we shall call the virtual machine for this bytecode language $\mathrm{VM}_{\mathrm{K}}$. This machine is similar to the JVM or the CLR. The instruction set is small to keep the logic simple, but large enough to show all the problems that may occur when trying to specify a more realistic bytecode language for an existing virtual machine. The formalisms for the bytecode logic closely follow [Ben04]. Objects and dynamic dispatch are treated similarly as in [PHM99]. It is not necessary to read these papers. We will repeat all the definitions, rules and conventions necessary to understand our ideas. ${ }^{1}$

Unlike the logic in [Ben04], we do not merge specification and typing information in our bytecode logic. But we naturally require certain well-typedness (see section 3.1.5 on page 14) conditions. This simple well-formedness can be checked by a "bytecode verifier". This separation of the verification process is necessary to keep the programming logic manageable: Properties that can be easily checked by a verifier are complicated enough ${ }^{2}$ that they should be reasoned about separately such that its result can form a basis for the more complicated behavioral correctness proofs of a program.

### 1.1 Why is a Bytecode Logic and a Corresponding Translation Procedure useful?

Is it necessary to have a logic for a bytecode programs? The ideas of proof-carrying code (PCC, [Nec97]) are already old. "Untrusted code" is augmented with information (the proof) that can render its (type-) safety checkable. The obligation to provide a proof for the safety of a program is deferred to the code producer. The only thing the user of the code has to do is checking the proof that comes with the program. Checking a proof is comparably simple. This procedure allows mobile code to be executed directly and without expensive runtime checks.

Unfortunately, properties described by PCC are typically limited to simple well-typedness of the binary code. PCC is used for hardware platforms, where well-typedness is not guaranteed. Because the proofs are simple, a certifying compiler can add a proof when compiling a program. For virtual machines like the JVM or the CLR, the bytecode verifier automatically ensures the type-safety of a program.

What is more, in order to show that programs and especially program components do the right things, it is just not enough to show that they do the things right, which is what PCC can guarantee. For complicated properties like adherence to an interface specification, we need a formal program proof. But program proofs are at most available on the level of the source code. They cannot be used for component- or class libraries that are shipped or sent over a network as bytecode.

Our arguments illustrate the necessity of two elements for a "proven components industry":

[^0]1. A logic for bytecode verification, to be able to show the correctness of bytecode programs.
2. An automatic translation from source code proofs to this bytecode logic. No one will be willing to prove programs on the bytecode level and manual intervention is still necessary for formal program verification.

The logic presented here and the accompanying translation procedure for program proofs is meant to be a first step towards a system that can automatically produce formally verified bytecode from a verified input program.

With introduction of the CLR, the idea of bytecode being a device for language interoperability has gained ground. By the definition of how source code is translated to bytecode, we define at the same time how source programs written in different languages can inter-operate. This fact defines another and important application of any bytecode and its corresponding translation procedures of source level proofs into that logic: To define a common semantics for specifications written in different languages. It can guarantee that correctness properties survive across language boundaries and are even formally accessible.

This text treats the very simple case of a source and target language that are directly comparable.
The sections of this paper are organized as follows: We will introduce you to the source logic for the subset of Java we are considering (taken from [PHM99]). We then define an operational semantics for the $\mathrm{VM}_{\mathrm{K}}$ bytecode. The operational semantics directly leads to the Hoare-style program logic for bytecode (our target calculus). The proof transformation procedure is explained in the following section and the paper ends with an example of a translation from a source proof to a bytecode proof. There is also a simple but complete implementation of the translation procedure given in section A on page 49.

We do not prove the soundness of our logic with respect to the operational semantics. A proof for a subset of the $\mathrm{VM}_{\mathrm{K}}$ instructions can be found in [Ben04]. The soundness proof of the remaining instructions should be straightforward, because there is a one-to-one correspondence between proof rules for the logic and transition rules of the operational semantics.

We tried to write the paper in a strongly intuitive style. The reader should be familiar however with the basic notions of programming language syntax and semantics.

## 2 The Logic for Sequential Java

In this section we introduce the Hoare-style logic that we translate from. It is a logic for a sequential Java kernel, called Java-K. We explain some of the concepts that are also used for the bytecode logic, e.g., how references and objects are handled. Only those parts of original paper ([PHM99]) are repeated that we need for the subsequent explanations of the $\mathrm{VM}_{\mathrm{K}}$ logic.

As always the intuitive meaning of the Hoare-triple

$$
\{P\} \text { comp }\{Q\}
$$

is that if $P$ holds in some initial state and the execution of comp terminates then $Q$ will hold in the halting state of comp.

We mainly concerned with the specification and verification of individual methods with pre- and postcondition: given some precondition of a method-body, what condition holds when the method terminates. The meaning of Hoare-triples ( $\{P\}$ Method body $\{Q\}$ ) can then be extended to method declaration identifiers that represent a method implementation or a set of method implementations in the case of a virtual
method. A statically bound method $m$ in class $T$ has the method identifier $T @ m$. A virtual method $m$ in class $T$ is denoted by $T: m$

Example 1. For a statically bound method $T @ m$

$$
\{P\} T @ m\{Q\}
$$

holds if

$$
\{P \wedge \text { this } \neq \text { null }\} \text { Method body of } T @ m\{Q\}
$$

We write $\operatorname{impl}(T @ m)$ for the method body of $T @ m$
Example 2. For a dynamically bound method $T: m$

$$
\{P\} T: m\{Q\}
$$

holds if

$$
\{P \wedge \text { this } \neq \text { null }\} \text { Method body of } S @ m\{Q\}
$$

holds for all subtypes $S$ of $T(S \preceq T)$, i.e. for all concrete implementations of $T: m$

The state of our programs consists of ${ }^{3}$ the local parameters, the local variables and the object store. Local parameters and local variables are treated identically, so we may say local variables if we mean local variables as well as parameters. The object store $\$$ is introduced in order to model the dynamic heap. The object store and some auxiliary functions together support the usual operations that are normally associated with the heap. These operations follow certain very intuitive axioms (given in section 3.1 of [PH97]). We do not treat them here, because they are only necessary to actually prove properties about your programs, but not to understand the concept of how the operations work and how they can be used.

- instance variable lookup:

$$
\text { iv : Value } \times \text { FieldDeclId } \rightarrow \text { InstVar }
$$

instance variables are written as Type@fieldname
Example 3. The class $T$ has the fields $a, b, c$ they are referred to as $T @ a, T @ b, T @ c$, resp.

- instance variable update:

$$
\$\langle f:=v\rangle: \text { ObjectStore } \times \text { InstVar } \times \text { Value } \rightarrow \text { ObjectStore }
$$

- instance variable load:

$$
\$(f): \text { ObjectStore } \times \text { InstVar } \rightarrow \text { Value }
$$

- new object allocation: this function returns the object-store after the allocation of a new object of type $T$

$$
\$\langle T\rangle: \text { ObjectStore } \times \text { ClassTypeId } \rightarrow \text { ObjectStore }
$$

- return a new object of type $T$

$$
\text { new }(\$, T): \text { ObjectStore } \times \text { ClassTypeId } \rightarrow \text { Value }
$$

Before we dive into the rules that enable us to handle method invocations and the object store, we will explain some peculiarities of Java-K and list the standard Hoare-rules that are required in almost every Hoare style logic.

[^1]
### 2.1 Conventions, Restrictions and Limitations of Java-K

The language Java-K follows some conventions and has a number of limitations:

- There is no abrupt termination:
- no break
- no continue
- no exception handling support
- no return statement: the return value of a method has to be assigned to a special variable result
the special variables this and result are treated just like any other parameter or local variable resp.
- No support for global data. This is not a real restriction as global variables can always be represented as fields of some metaclass objects. This is not practically useful in the restricted context of specifications that consists only of pre- and postconditions of method implementations and is therefore omitted.
- While there can be basically many parameters, we give rules for methods with only one formal parameter p . All in all, we usually have two parameters: the customary this and p .

For an example on how some of these rules are used in a proof, see section 3.2 on page 15 .

### 2.2 Base Rules

There are a few rules common to practically every Hoare-style logic. These rules are taken from figure 1 of [PHM99] and are presented here without comment.

### 2.2.1 assign

$$
\operatorname{assign} \overline{\{P[e / x]\} \quad x=e\{P\}}
$$

### 2.2.2 if

$$
\begin{aligned}
& \{e=\top \wedge P\} \operatorname{comp}_{1}\{Q\} \\
& \{e=\perp \wedge P\} \operatorname{comp}_{2}\{Q\} \\
& \operatorname{if}(e)\left\{\operatorname{comp}_{1}\right\} \operatorname{else}\left\{\operatorname{comp}_{2}\right\}\{Q\}
\end{aligned}
$$

### 2.2.3 seq

$$
\begin{aligned}
& \{P\} \operatorname{comp}_{1}\{R\} \\
& \{R\} \operatorname{comp}_{2}\{Q\} \\
& \} \operatorname{comp}_{1} ; \operatorname{comp}_{2}\{Q\}
\end{aligned}
$$

### 2.2.4 while

$$
\text { while } \frac{\{e=\top \wedge P\} \operatorname{comp}\{P\}}{\{P\} \text { while }(e)\{\operatorname{comp}\}\{Q\}}
$$

2.2.5 conjunct

$$
\text { conjunct } \frac{\left\{P_{1}\right\} \text { comp }\left\{Q_{1}\right\}}{\left\{P_{2}\right\} \operatorname{comp}\left\{Q_{2}\right\}} \begin{aligned}
& \left\{P_{1} \wedge P_{2}\right\} \operatorname{comp}\left\{Q_{1} \wedge Q_{2}\right\}
\end{aligned}
$$

### 2.2.6 disjunct

$$
\text { disjunct } \frac{\left\{P_{1}\right\} \text { comp }\left\{Q_{1}\right\}}{\left\{P_{2}\right\} \text { comp }\left\{Q_{2}\right\}}
$$

### 2.2.7 strength

$$
\begin{gathered}
P \Rightarrow P^{\prime} \\
\text { strength } \frac{\left\{P^{\prime}\right\} \operatorname{comp}\{Q\}}{\{P\} \operatorname{comp}\{Q\}}
\end{gathered}
$$

### 2.2.8 weak

$$
\begin{gathered}
Q^{\prime} \Rightarrow Q \\
\text { weak } \frac{\{P\} \operatorname{comp}\left\{Q^{\prime}\right\}}{\{P\} \operatorname{comp}\{Q\}}
\end{gathered}
$$

2.2.9 inv
$R$ doesn't contain references to the program state


### 2.2.10 subst

$t$ doesn't contain references to the program state $Z$ is a logical variable


### 2.2.11 all

$$
\begin{gathered}
Z, Y \text { are distinct logical variables } \\
\text { all } \frac{\{P[Y / Z]\} \operatorname{comp}\{Q\}}{\{P[Y / Z]\} \operatorname{comp}\{\forall Z: Q\}}
\end{gathered}
$$

### 2.2.12 ex

$$
\begin{gathered}
Z, Y \text { are distinct logical variables } \\
\text { ex } \frac{\{P\} \operatorname{comp}\{Q[Y / Z]\}}{\{\exists Z: P\} \operatorname{comp}\{Q[Y / Z]\}}
\end{gathered}
$$

### 2.3 Rules for References, Objects and Dynamic Dispatch

We are now going to examine the rules that are necessary to support object-oriented features. We begin with the most straightforward rules and progress to the more sophisticated ones. ( $\tau(x)$ denotes the run-time-type of $x$ )

### 2.3.1 cast

$$
\operatorname{cast} \overline{\{\tau(e) \preceq T \wedge P[e / x]\} \quad x=(T) e\{P\}}
$$

The cast-axiom is similar to the classical assignment axiom, but the stronger precondition ensures that the cast cannot fail.

### 2.3.2 construction

$$
\text { construction } \overline{\{P[\operatorname{new}(\$, T) / x, \$\langle T\rangle / \$]\} x=\operatorname{new} T()\{P\}}
$$

The construction axiom allocates a new object on the heap and assigns this newly created object to $x$. Keep in mind that the object-store $\$$ is the entity that allows us to track the state of the heap syntactically.

### 2.3.3 fieldread

$$
\text { fieldread } \overline{\{y \neq \operatorname{null} \wedge P[\$(\operatorname{iv}(y, S @ a)) / x]\} x=y \cdot S @ a\{P\}}
$$

This is a direct adaptation of the assign-axiom.

### 2.3.4 fieldwrite

$$
\text { fieldwrite } \overline{\{y \neq \operatorname{null} \wedge P[\$\langle\operatorname{iv}(y, S @ a):=e\rangle]\} \quad y . S @ a=e\{P\}}
$$

This is a direct adaptation of the assign-axiom as well.

### 2.3.5 invocation

$$
\text { invocation } \frac{\{P\} T: m\{Q\}}{\{y \neq \text { null } \wedge P[y / \text { this }, e / \mathrm{p}]\} x=y \cdot T: m(e)\{Q[x / \text { result }]\}}
$$

The effect of a method invocation is the same as the effect of the method body expanded at the call site with the formal replaced by the actual parameters and the result-variable in the method replaced by the actual local variable that should receive that result value. Because we do not know which actual method implementation we are calling, we take the Hoare-triple that holds for all bodies (implementations) of $T: m$ (i.e. $\{P\} T: m\{Q\}$ )

### 2.3.6 invocation-var

$$
\begin{gathered}
w \text { is a program variable } \\
Z \text { is a logical variable } \\
w \neq x \\
\text { invocation-var } \frac{\{P\} x=y \cdot T: m(e)\{Q\}}{\{P[w / Z]\} x=y \cdot T: m(e)\{Q[w / Z]\}}
\end{gathered}
$$

This rule formalizes that local variables $(w)$ are not modified by a function invocation.

### 2.3.7 implementation

$$
\text { implementation } \frac{\{\text { this } \neq \text { null } \wedge P\} \text { body }(T @ m)\{Q\}}{\{P\} T @ m\{Q\}}
$$

This rule formalizes what we have said before: The meaning of a function body can be transferred to its signature. For the proof of the body, we can assume that $\{P\} T @ m\{Q\}$ holds (this is necessary for recursive procedures). In [PHM99] there are also formal rules that link the specifications of a virtual method to the actual implementations of that method. They allow statements to be made about virtual method identifiers:

$$
\cdots \frac{\cdots}{\{P\} T: m\{Q\}}
$$

we do not consider such rules in our text. We just note that it is possible to deduce statements $\{P\} T: m\{Q\}$ if we know $\{P\} S @ m\{Q\} \forall S \preceq T$

## 3 The $\mathrm{VM}_{\mathrm{K}}$ bytecode

In this section, we're introducing the design of $\mathrm{VM}_{\mathrm{K}}$ virtual machine bytecode language. As indicated in the introduction, $\mathrm{VM}_{\mathrm{K}}$ is a machine

- with unrestricted control flow expressed using conditional and unconditional jumps to labeled instructions within a method body.
- $\mathrm{VM}_{\mathrm{K}}$ is stack based. It does not impose any limit on the elements that may be pushed onto the stack.

We restrict our attention to the specification and verification of individual method implementations by the means of simple pre- and post-conditions for that method. This is the same abstraction as for Java-K. In

Java-K the method bodies consist of a statement. In $\mathrm{VM}_{\mathrm{K}}$ they consist of sequences of instructions with labels. We therefore have to reason about these sequences.

Because the specification of a program consists only of a pre- and a post-condition of the method, we can use the same abstractions for methods ${ }^{4}$ as we have used for Java-K. One of the first goals of this section is therefore to link the two entities we are dealing with: specifications of method identifiers and the conformance of actual implementations to these specifications. I.e., we need a $\mathrm{VM}_{\mathrm{K}}$-rule corresponding to the implementation rule for Java-K:

$$
\text { implementation } \frac{\{\text { this } \neq \text { null } \wedge P\} \text { body }(T @ m)\{Q\}}{\{P\} T @ m\{Q\}}
$$

Unfortunately, we cannot directly adapt this rule from Java-K, because our $\mathrm{VM}_{\mathrm{K}}$ method body consists not of a single computation, but of a sequence of individual instructions. To be able to give an implementation rule for $\mathrm{VM}_{\mathrm{K}}$, we have to define first what

$$
\{\text { this } \neq \text { null } \wedge P\} \text { body }(T @ m)\{Q\}
$$

means for a body(T@m) being a sequence of bytecode instructions. From now on, we write body $y_{\mathrm{VM}_{\mathrm{K}}}(T @ m)$ to indicate that we refer to the method body in bytecode form.

As we have jumps in our program, we can easily verify only one bytecode instruction at a time and we say that a sequence $\mathfrak{p}$ of instructions is well specified if all of its components are: The following definitions formalize this idea step by step. We define what $\mathrm{VM}_{\mathrm{K}}$ instructions are, how they can be specified, what they do and how they can be assembled to specified instruction sequences. It is then easy to see how the $\mathrm{VM}_{\mathrm{K}}$ implementation rule can be constructed.

Definition 1. A specified $\mathrm{VM}_{\mathrm{K}}$ instruction consists of

1. an instruction
```
instr \in({pushc c, pushvar x, pop x, binop op, unop op , brtrue l', goto l'}
    \cup{checkcast T, invokevirtual T:m,getfield T:a,putfield T:a,nop, end_method})
```

2. a label $l \in \mathbb{N}$ (unique in the instruction sequence that contains the instruction)
3. a precondition $E_{l}$
4. a postcondition $E_{l+1}$

We write this specified instruction as $\left\{E_{l}\right\} l: \operatorname{instr}\left\{E_{l+1}\right\}$
The meaning of $\left\{E_{l}\right\} l$ : instr $\left\{E_{l+1}\right\}$ can not been defined in isolation when instr is a control transferring instruction. What we can say however is that if $E_{l}$ holds when the program counter is just before the instruction (at position $l$ ) and instr does not transfer control (instr $\notin\left\{\right.$ goto, brtrue $\left.l^{\prime}\right\}$ ) then $E_{l+1}$ will hold after the successful execution of instr. We will define the meaning of general instruction specifications together with the definition of full method body specifications.

We can prove (verify) a specified instruction $\left\{E_{l}\right\} l: \operatorname{instr}\left\{E_{l+1}\right\}$ by giving a proof tree for it.
Definition 2. The operational semantics is normative, but this definition gives an informal overview of the instructions available in the $\mathrm{VM}_{\mathrm{K}}$.

[^2]- pushc $v$ pushes a constant $v$ onto the stack
- pushvar $x$ pushes the value of a local variable (or method parameter) onto the stack
- pop $x$ pops the top element off the stack and assigns it to the local variable x
- unop $p_{\text {op }}$ replaces the top element by applying the unary operation op to it
- binop ${ }_{\mathrm{op}}$ replaces the two top elements of the stack by applying the binary operation op to them
- goto $l$ transfers control the program point $l$
- brtrue $l$ transfers control the program point $l$ if the top element of the stack is true and unconditionally pops it.
- checkcast $T$ checks whether the top element is of type $T$ or a subtype thereof.
- newobj $T$ allocates a new object of type $T$ and pushes it onto the stack
- invokevirtual $T: m$ invokes the method $T: m$ on the object reference that is the second-topmost element with the parameter that is the topmost element and replaces these two values by the return value of the invoked method $T: m$
- getfield $T @ a$ replaces the top element by its field $T @ a$
- putfield $T @ a$ sets the field $T @ a$ of the object denoted by the second-topmost element to the top element of the stack and pops both values.
- nop has no effect
- end_method halts the execution. The intuition is that it transfers control back to the invoking method. It serves therefore as an end marker for a method. There must be exactly one end_method instruction per method body at the end of the method.

Definition 3. A specified $\mathrm{VM}_{\mathrm{K}}$ method implementation $\mathfrak{p}$ consists of a sequence of specified $\mathrm{VM}_{\mathrm{K}}$ instructions.

1. $|\mathfrak{p}|$ is the number of instructions in the method
2. The labels of the instructions are in $\Lambda_{\mathfrak{p}}=\{1 . .|\mathfrak{p}|\}$
3. For every label, we can retrieve the corresponding instruction, pre- and postcondition.

$$
\begin{aligned}
\mathfrak{p}(l) & =I_{l} \\
\operatorname{precondition}_{\mathfrak{p}}(l) & =E_{l} \\
\operatorname{postcondition}_{\mathfrak{p}}(l) & =E_{l+1} \\
\operatorname{spec}_{\mathfrak{p}}(l) & =\left\{E_{l}\right\} l: I_{l}\left\{E_{l+1}\right\}
\end{aligned}
$$

4. $\mathfrak{p}$, precondition $\mathfrak{p}_{\mathfrak{p}}$, postcondition $_{\mathfrak{p}}, \operatorname{spec}_{\mathfrak{p}}$ are functions, i.e. there is exactly one instruction, precondition, postcondition for every label in a method body. Putting it differently, labels are unique within an instruction sequence.
5. The postcondition of one instruction is the precondition of the next

$$
\operatorname{postcondition}_{\mathfrak{p}}(l)=\operatorname{precondition}_{\mathfrak{p}}(l+1)
$$

The meaning of $\left\{E_{l}\right\} l: \operatorname{instr}\left\{E_{l+1}\right\}$ in such a instruction sequence is that if $E_{l}$ holds when the program counter is just before the instruction (at position $l$ ) then the precondition $E_{l^{\prime}}$ of the successor instruction at label $l^{\prime}$ will also hold after successful termination of instruction $l$.

Definition 4. A specified method implementation $\mathfrak{p}$ can be verified by verifying all of its components. The precondition for $\mathfrak{p}$ is the precondition of the first instruction, the postcondition of $\mathfrak{p}$ is the postcondition of the last instruction, which, moreover, is of kind end_method. It is the only end_method instruction.

$$
\begin{aligned}
I_{|\mathfrak{p}|} & =\text { end_method } \\
{\left[\forall i \in \Lambda_{\mathfrak{p}}: \operatorname{spec}_{\mathfrak{p}}(i)\right] \quad \begin{aligned}
& \\
& \forall i<|\mathfrak{p}|: \mathfrak{p}(i) \neq \text { end_method } \\
& \text { precondition } \\
& \mathfrak{p}
\end{aligned}(1) } & =P \\
\text { postcondition }(|\mathfrak{p}|) & =Q
\end{aligned}
$$

Definition 5. We can verify the specification of a method $T @ m$ by verifying the method implementation $\mathfrak{p}=b o d y_{\mathrm{VM}_{\mathrm{K}}}(T @ m)$.

$$
\mathrm{VM}_{\mathrm{K}} \text {-implementation- } 0 \frac{\{\text { this } \neq \text { null } \wedge P\} \text { bod } y_{\mathrm{VM}_{\mathrm{K}}}(T @ m)\{Q\}}{\{P\} T @ m\{Q\}}
$$

And because there is exactly one rule we necessarily have to apply to bod $y_{\mathrm{VM}_{\mathrm{K}}}(T @ m)$ we combine the body and the implementation-0 rule:

$$
\begin{aligned}
I_{|\mathfrak{p}|} & =\text { end_method } \\
\mathfrak{p}=\text { bod } y_{\mathrm{VM}_{\mathrm{K}}}(T @ m) & {\left[\forall i \in \Lambda_{\mathfrak{p}}: \operatorname{spec}_{\mathfrak{p}}(i)\right] \quad \begin{aligned}
& \\
& \mathrm{VM}_{\mathrm{K}} \begin{aligned}
& \\
& \operatorname{precondition}_{\mathfrak{p}}(1)=P \wedge \text { this } \neq \text { null } \\
& \operatorname{postcondition~}_{\mathfrak{p}}(|\mathfrak{p}|)=Q
\end{aligned} \\
&\{P\} T @ m\{Q\}
\end{aligned} }
\end{aligned}
$$

Remember that proofs in [PHM99] are not only about statements but also about method identifiers. We need this functionality as well for our logic as we are interested in translating Java-K proofs. We will therefore include all the rules of the Java-K logic in the $\mathrm{VM}_{\mathrm{K}}$ logic and leave the proofs for other program entities than method implementations as is. We do however not use the usual Java-K rules for verifying method bodies.

Our translation procedure is then limited to translating method implementation proofs for Java-K to method implementation proofs for $\mathrm{VM}_{\mathrm{K}}$. Parts of the derivation trees outside the method bodies stay the same:

$$
\frac{\{\text { this } \neq \text { null } \wedge P\} \text { body }(T @ m)\{Q\}}{\{P\} T @ m\{Q\}} \Leftrightarrow \mathrm{vM}_{\mathrm{K}} \frac{\ldots}{\{P\} T @ m\{Q\}}
$$

There are the same restrictions for our $\mathrm{VM}_{\mathrm{K}}$ as for Java-K: see section 2.1 on page 5 .

With these definitions, we have a simple goal: to give a sound and complete system of rules that allow to prove pre- and postconditions for individual instructions. Before we can do that, we should know what our instructions are and what they are supposed to do, i.e., what their effect on the program state is. We will therefore define an operational semantics of all instructions first, and then give rules for proving preand postconditions about them.

### 3.1 Operational Semantics

Definition 6. The configuration $\langle S, \sigma, l\rangle$ of a program during execution consists of the program environment $S$, the stack $\sigma$ and the program counter $l$ (the label of the next instruction to be executed).

- The State $S$ maps variables and parameters to values and $\$$ to the current object store

$$
\begin{aligned}
S & \in \text { State } \\
\text { State } & \equiv(\text { LocalVariable } \cup\{\text { this }, p\} \rightarrow \text { Value }) \times(\{\$\} \rightarrow \text { ObjectStore })
\end{aligned}
$$

- The stack is a list of values

$$
\begin{gathered}
\sigma \in \text { Stack } \\
\text { Stack } \equiv \text { 三Value } *
\end{gathered}
$$

- The program counter is always at a valid position

$$
l \in \Lambda_{\mathfrak{p}}
$$

Definition 7. The small step transition

$$
\mathfrak{p} ;\langle S, \sigma, l\rangle \rightarrow\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle
$$

means that for the program $\mathfrak{p}$, the machine can go in one step from the state $\langle S, \sigma, l\rangle$ to the $\mathrm{VM}_{\mathrm{K}}$ state $\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle$.
we also define the multistep relation $\mathfrak{p} ;\langle S, \sigma, l\rangle \rightarrow^{*}\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle$ inductively exactly as in [Ben04]:

$$
\mathfrak{p} ;\langle S, \sigma, l\rangle \rightarrow^{*}\langle S, \sigma, l\rangle
$$

The $\rightarrow^{*}$ multistep relation frees us from having to model procedure activation frames explicitly.

$$
\frac{\mathfrak{p} ;\langle S, \sigma, l\rangle \rightarrow\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle \quad \mathfrak{p} ;\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle \rightarrow{ }^{*}\left\langle S^{\prime \prime}, \sigma^{\prime \prime}, l^{\prime \prime}\right\rangle}{\mathfrak{p} ;\langle S, \sigma, l\rangle \rightarrow^{*}\left\langle S^{\prime \prime}, \sigma^{\prime \prime}, l^{\prime \prime}\right\rangle}
$$

Equipped with these definitions, we can now define the individual instructions of our virtual machine. See partition II of [ECM02] and [LY99] to compare them with the actual instructions of our example VMs.

Also note that we do not have subroutines ( j sr in the JVM) although they would be easy to add. We do not need them because our source language does not support exception handling (which is what subroutines are used for in the JVM).

### 3.1.1 Instructions for the Compilation of Expressions

Pushing a Constant onto the Stack: pushc $v$ The instruction pushc $v$ is defined by the transition

$$
[\ldots l: \text { pushc } v \ldots] ;\langle S, \sigma, l\rangle \rightarrow\langle S,(\sigma, v), l+1\rangle
$$

Pushing the Value of a Local Variable onto the Stack: pushvar $x$ The instruction pushvar $x$ is defined by the transition

$$
[\ldots l: \text { pushvar } x \ldots] ;\langle S, \sigma, l\rangle \rightarrow\langle S,(\sigma, S(x)), l+1\rangle
$$

Popping the Stack into a Local Variable: pop $x$ The instruction pop $x$ is defined by the transition

$$
[\ldots l: \operatorname{pop} x \ldots] ;\langle S,(\sigma, v), l\rangle \rightarrow\langle S[x \mapsto v], \sigma, l+1\rangle
$$

Unary Operations: unop $_{\mathrm{op}}$ The instruction unop ${ }_{\mathrm{op}}$ is defined by the transition

$$
\left[\ldots l: \text { unop }_{\mathrm{op}} \ldots\right] ;\langle S,(\sigma, v), l\rangle \rightarrow\langle S,(\sigma, \text { op } v), l+1\rangle
$$

Binary Operations: binop $_{\text {op }}$ The instruction binop ${ }_{o p}$ is defined by the transition

$$
\left[\ldots l: \operatorname{binop}_{\text {op }} \ldots\right] ;\left\langle S,\left(\sigma, v_{1}, v_{2}\right), l\right\rangle \rightarrow\left\langle S,\left(\sigma, v_{1} \text { op } v_{2}\right), l+1\right\rangle
$$

### 3.1.2 Instructions that Modify the Control Flow

Unconditional Jump: goto $l^{\prime}$ The instruction goto $l^{\prime}$ is defined by the transition

$$
\left[\ldots l: \text { goto } l^{\prime} \ldots\right] ;\langle S, \sigma, l\rangle \rightarrow\left\langle S, \sigma, l^{\prime}\right\rangle
$$

Conditional Jump: brtrue $l^{\prime}$ The instruction brtrue $l^{\prime}$ is defined by the transitions

$$
\left[\ldots l: \text { brtrue } l^{\prime} \ldots\right] ;\langle S,(\sigma, \top), l\rangle \rightarrow\left\langle S, \sigma, l^{\prime}\right\rangle
$$

and

$$
\left[\ldots l: \text { brtrue } l^{\prime} \ldots\right] ;\langle S,(\sigma, \perp), l\rangle \rightarrow\langle S, \sigma, l+1\rangle
$$

### 3.1.3 Instructions for Objects

Casting a Reference: checkcast $T$ The instruction checkcast $T$ is defined by the transition

$$
\frac{\tau(v) \preceq T}{[\ldots l: \text { checkcast } T \ldots] ;\langle S,(\sigma, v), l\rangle \rightarrow\langle S,(\sigma, v), l+1\rangle}
$$

Object Creation: newobj $T$ The instruction newobj $T$ is defined by the transition

$$
[\ldots l: \text { newobj } T \ldots] ;\langle S, \sigma, l\rangle \rightarrow\langle S[\$ \mapsto S(\$)\langle T\rangle],(\sigma, \operatorname{new}(S(\$), T))), l+1\rangle
$$

Calling a Virtual Method (with Exactly one Argument): invokevirtual $T: m$ The instruction invokevirtual $T: m$ is defined by the transition

$$
\begin{gathered}
\mathfrak{p}^{\prime}=\operatorname{body}_{\mathrm{VM}_{\mathrm{K}}}(\operatorname{impl}(\tau(y), m)) \quad \mathfrak{p}^{\prime}\left(l^{\prime}\right)=\text { end method } \\
\mathfrak{p}^{\prime} ;\langle\{\text { this } \mapsto y, \mathrm{p} \mapsto v, \$ \mapsto S(\$)\},(), 0\rangle \rightarrow^{*}\left\langle\bar{S}^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle \\
{[\ldots l: \text { invokevirtual } T: m \ldots] ;\langle S,(\sigma, y, v), l\rangle \rightarrow\left\langle S\left[\$ \mapsto S^{\prime}(\$)\right],\left(\sigma, S^{\prime} \text { (result) }\right), l+1\right\rangle}
\end{gathered}
$$

Loading a Field of an Object: getfield $T @ a$ The instruction getfield $T @ a$ is defined by the transition

$$
\frac{y \neq \text { null }}{[\ldots l: \text { getfield } T @ a \ldots] ;\langle S,(\sigma, y), l\rangle \rightarrow\langle S,(\sigma, S(\$)(\operatorname{iv}(y, T @ a)), l+1\rangle}
$$

Storing into a Field of an Object: putfield $T @ a$ The instruction putfield $T @ a$ is defined by the transition

$$
\frac{y \neq \text { null }}{[\ldots l: \text { putfield } T @ a \ldots] ;\langle S,(\sigma, y, v), l\rangle \rightarrow\langle S[\$ \mapsto S(\$)\langle\operatorname{iv}(y, T @ a):=v\rangle], \sigma, l+1\rangle}
$$

### 3.1.4 Additional Instructions

No Operation: nop The instruction nop is defined by the transition

$$
[\ldots l: \text { nop } \ldots] ;\langle S, \sigma, l\rangle \rightarrow\langle S, \sigma, l+1\rangle
$$

### 3.1.5 Comments

Observation 1. The operational semantics is deterministic.
There is at most one transition for every statement and program point. And program points are unique in a program.

Observation 2. There should be constraints on the state at a given program point.
Example 4. The effect of an execution step starting in the configuration $[\ldots l$ : pushvar $x \ldots] ;\left\langle S^{\prime}, \sigma, l\right\rangle$ cannot be reasonably legitimated if the variable $x$ is not yet initialized.

The configuration $\left[\ldots l:\right.$ binop $\left._{\text {op }} \ldots\right] ;\left\langle S^{\prime},(\sigma, a, b), l\right\rangle$ should not have a successor if op is not an operation on $\tau(a) \times \tau(b) \rightarrow \alpha$ for some type $\alpha$.

We assume that every $\mathrm{VM}_{\mathrm{K}}$ program satisfies some basic well-formedness constraints that ensure that such situations can never occur.

We do so not only because this is rarely prohibitive and quite usual for real machines - both the JVM and the CLR have bytecode verifiers - but also because this additional abstraction helps us keep the logic we will construct simple. We are ruling out invalid behavior (type errors, popping the empty stack, ...)
of our programs before our logic is applied to them. An alternative approach would be to combine type checking ${ }^{5}$ and verification. Compare [Ben04] on how this can be done.

It should be noted that type-checking bytecode is itself a non-trivial undertaking: Research on this topic has led to the publication of a vast quantity of articles and several Ph.D. theses. It is only recently that the implications of the complex interplay between unrestricted control flow, exception handling and unstructured subroutines has been completely understood ${ }^{6}$. The approach taken in [Ben04] is therefore unlikely to scale to the extended instruction set of a real virtual machine.

### 3.2 An Example: Calculating $x^{n}$ Recursively

The sample code has been compiled by the Sun JDK 1.4.2 compiler and has been transcribed for the $\mathrm{VM}_{\mathrm{K}}$.

### 3.2.1 Java-K

```
package pow;
public class Recursiv{
        public int pow(int x, int n) {
            if(n == 0) {
            result = 1;
        }else {
                if(n%2 == 0) {
                result = this.pow(x*x, n/2);
            }else {
                result = this.pow(x, n-1);
                result = result*x;
            }
        }
    }
}
```

Listing 1: Recursive calculation of $x^{n}$ in Java-K: This is an example of a complete Java-K program. The Java-K method as well as the corresponding bytecode method implementation (section 3.2.2) are later verified (see section 3.5 .1 on page 19 for the Java-K verification).

### 3.2.2 $\quad \mathrm{VM}_{\mathrm{K}}$

```
    pushc 1
    pop result
test-1: pushvar n
    unop <>0,
    brtrue path-2
path-1: pushc 1
    pop result
    goto end
test-2: pushvar n
    pushc 2
    binop "%"
unop <>0
```

[^3]```
    brtrue path-3
path-2: pushvar this
    pushvar x
    pushvar x
    binop "*"
    pushvar n
    pushc 2
    binop "/"
    invokevirtual Recursiv:pow
    pop result
    goto end
path-3: pushvar x
    pushvar this
    pushvar x
    pushvar n
    pushc 1
    binop "-"
    invokevirtual Recursiv:pow
    binop "*"
    pop result
end: end_method
```

Listing 2: Recursive calculation of $x^{n}$ in $\mathrm{VM}_{\mathrm{K}}$ bytecode: This the translation of the method body of the corresponding Java-K implementation (section 3.2.1 on the previous page). Both implementations are verified in section 3.5 .1 on page 19 and section 3.5 .2 on page 20 .

### 3.3 A Programming Logic for the Virtual Machine

In the next section 3.4 we are finally going to present the rules of our logic for the $\mathrm{VM}_{\mathrm{K}}$ bytecode. Having done that, we will verify the specification of the running example Recursiv@pow. (section 3.2.2 on the previous page)
Definition 8. The current stack is referred to as $s$, and its elements are denoted by non-negative integers: element 0 is the top element, etc.:

$$
\begin{aligned}
\llbracket s(0) \rrbracket\langle S,(\sigma, v), l\rangle & =v \\
\llbracket s(i+1) \rrbracket\langle S,(\sigma, v), l\rangle & =\llbracket s(i) \rrbracket\langle S, \sigma, l\rangle
\end{aligned}
$$

Definition 9. Substitutions $E\left[e^{\prime} / x\right]$ or $E\left[e^{\prime} / s(i)\right]$, the simultaneous substitutions $E\left[e_{1}^{\prime} / z_{1}, e_{2}^{\prime} / z_{2}, \ldots\right]$ and the evaluation 【.』 of assertions $E_{l}$ are defined as usual. We do not want to restrict the assertions that can be made and we therefore do not define them here (except for the access to stack elements in the previous definition).
Definition 10. Helper functions

$$
\begin{aligned}
\operatorname{shift}(E) & =E[s(i+1) / s(i) \text { for all } i \in \mathbb{N}] \\
\text { unshift } & =\operatorname{shift}^{-1}
\end{aligned}
$$

### 3.4 The Rules

With the explanations at the beginning of this section (section 3 on page 8 ) and the operational semantics at hand, we can define the rules for individual instructions.

Observation 3. There is exactly one transition for every instruction in our operational semantics and exactly one rule in our programming logic. It is no surprise that they look similar.

This one-to-one correspondence can help us prove the soundness of the logic: we just need to show that if there is a small step transition from one state $K=\langle S, \sigma, l\rangle$ to its next state $K^{\prime}=\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle^{7}$ and the precondition $E_{l}$ holds in $K$ then $E_{l^{\prime}}$ must also hold in $K^{\prime}$.

We may have reached the end of the method if there is no transition. I.e. we may have reached the end_method instruction. In that case, we know the the precondition of end_method holds. This is also the postcondition of our method. Because the end_method instruction is the only means of returning from a method ${ }^{8}$, we know that if a method terminates, its postcondition will always hold.

### 3.4.1 Instructions for Expressions

$$
\begin{aligned}
& \text { pushc } \frac{\operatorname{shift}\left(E_{l}\right) \rightarrow E_{l+1}[v / s(0)]}{\left\{E_{l}\right\} l: \text { pushc } v\left\{E_{l+1}\right\}} \\
& \text { pushvar } \frac{\operatorname{shift}\left(E_{l}\right) \rightarrow E_{l+1}[x / s(0)]}{\left\{E_{l}\right\} l: \text { pushvar } x\left\{E_{l+1}\right\}} \\
& \operatorname{pop} \frac{E_{l} \rightarrow\left(\operatorname{shift}\left(E_{l+1}\right)\right)[s(0) / x]}{\left\{E_{l}\right\} l: \operatorname{pop} x\left\{E_{l+1}\right\}} \\
& \operatorname{unop} \frac{E_{l} \rightarrow E_{l+1}[(\operatorname{op} s(0)) / s(0)]}{\left\{E_{l}\right\} l: \text { unop }_{\text {op }}\left\{E_{l+1}\right\}} \\
& \operatorname{binop} \frac{E_{l} \rightarrow\left(\operatorname{shift}\left(E_{l+1}\right)\right)[(s(1) \text { op } s(0)) / s(1)]}{\left\{E_{l}\right\} l: \text { binop }_{\text {op }}\left\{E_{l+1}\right\}}
\end{aligned}
$$

### 3.4.2 Instructions that Modify the Control Flow

$$
\begin{gathered}
\text { goto } \frac{E_{l} \rightarrow E_{l^{\prime}}}{\left\{E_{l}\right\} l: \text { goto } l^{\prime}\left\{E_{l+1}\right\}} \\
\text { brtrue } \frac{E_{l} \wedge \neg s(0) \rightarrow \operatorname{shift}\left(E_{l+1}\right) \quad E_{l} \wedge s(0) \rightarrow \operatorname{shift}\left(E_{l^{\prime}}\right)}{\left\{E_{l}\right\} l: \text { brtrue } l^{\prime}\left\{E_{l+1}\right\}}
\end{gathered}
$$

### 3.4.3 Instructions for Objects

$$
\begin{gathered}
\text { checkcast } \frac{E_{l} \rightarrow E_{l+1} \wedge \tau(s(0)) \preceq T}{\left\{E_{l}\right\} l: \text { checkcast } T\left\{E_{l+1}\right\}} \\
\text { newobj } \frac{\operatorname{shift}\left(E_{l}\right) \rightarrow E_{l+1}[\text { new }(\$, T) / s(0), \$\langle T\rangle / \$]}{\left\{E_{l}\right\} l: \text { newobj } T\left\{E_{l+1}\right\}} \\
\text { getfield } \frac{E_{l} \rightarrow E_{l+1}[\$(\operatorname{iv}(s(0), T @ a)) / s(0)] \wedge s(0) \neq \text { null }}{\left\{E_{l}\right\} l: \text { getfield } T @ a\left\{E_{l+1}\right\}}
\end{gathered}
$$

[^4]$$
\text { putfield } \frac{E_{l} \rightarrow\left(\operatorname{shift}^{2}\left(E_{l+1}\right)\right)[\$\langle\operatorname{iv}(s(1), T @ a):=s(0)\rangle / \$] \wedge s(1) \neq \text { null }}{\left\{E_{l}\right\} l: \text { putfield } T @ a\left\{E_{l+1}\right\}}
$$
for exactly one argument, this is what we are going to reason about
$\{P\} T: m\{Q\}$
invokevirtual $\frac{E_{l} \rightarrow s(1) \neq \text { null } \wedge P[s(1) / \text { this, } s(0) / \mathrm{p}] \quad Q[s(0) / \text { result }] \rightarrow E_{l+1}}{\left\{E_{l}\right\} l: \text { invokevirtual } T: m\left\{E_{l+1}\right\}}$
$Z$ is a logical variable
$w$ is a local variable or a stack element $\neq s(0)$
$$
\text { invokevar } \frac{\left\{E_{l}^{\prime}\right\} l: \text { invokevirtual } T: m\left\{E_{l+1}^{\prime}\right\} \quad E_{l} \rightarrow E_{l}^{\prime}[(\operatorname{shift}(w)) / Z] \quad E_{l+1}^{\prime}[w / Z] \rightarrow E_{l+1}}{\left\{E_{l}\right\} l: \text { invokevirtual } T: m\left\{E_{l+1}\right\}}
$$
or more generally for an arbitrary number $n$ of arguments
\[

$$
\begin{gathered}
\{P\} T: m\{Q\} \\
\text { invokevirtual-n } \begin{array}{c}
\left\{\begin{array}{l}
E_{l} \rightarrow s(1) \neq \text { null } \wedge P\left[s(n) / \text { this, } \forall i \in 1 . . n: s(n-i) / \mathrm{p}_{i}\right] \quad Q[s(0) / \text { result }] \rightarrow E_{l+1} \\
\left\{E_{l}\right\} l: \text { invokevirtual } T: m\left\{E_{l+1}\right\}
\end{array}\right. \\
Z \text { is a logical variable } \\
w \text { is a local variable or a stack element } \neq s(0) \\
E_{l} \rightarrow E_{l}^{\prime}\left[\left(\operatorname{sifh}^{n}(w)\right) / Z\right] \quad E_{l+1}^{\prime}[w / Z] \rightarrow E_{l+1} \\
\left\{E_{l}^{\prime}\right\} l: \text { invokevirtual } T: m\left\{E_{l+1}^{\prime}\right\}
\end{array} \\
\text { invokevar-n } \frac{\left\{E_{l}\right\} l: \text { invokevirtual } T: m\left\{E_{l+1}\right\}}{}
\end{gathered}
$$
\]

### 3.4.4 Other Instructions

$$
\begin{gathered}
\operatorname{nop} \frac{E_{l} \rightarrow E_{l+1}}{\left\{E_{l}\right\} l: \text { nop }\left\{E_{l+1}\right\}} \\
\operatorname{method} \frac{E_{l} \rightarrow E_{l+1}}{\left\{E_{l}\right\} l: \text { end_method }\left\{E_{l+1}\right\}}
\end{gathered}
$$

### 3.5 An Example: Calculating $x^{n}$

This section continues section 3.2 on page 15 . Here we specify and verify the bytecode method presented there. The specification is:

$$
\left\{P \equiv x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\} \text { Recursiv@pow }\left\{Q \equiv \text { result }=x_{0}^{n_{0}}\right\}
$$

An assumption during verification is $\{P\}$ Recursiv : pow $\{Q\}$. See [PHM99], for exactly when such an assumption can be deduced from the assumption $\{P\}$ Recursiv@pow $\{Q\}$. The reasoning is simple in our case because Recursiv is the only class in our program.

We also list a Java-K proof to allow comparison with the proof system we ultimately want to translate from.

### 3.5.1 Java-K

$\left\{\right.$ this $\neq$ null $\left.\wedge x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}$

$$
\Longrightarrow
$$

$$
\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}
$$

$$
\text { if }(n==0)\{
$$

$$
\left\{n=0 \wedge x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}
$$

$$
\text { result }=1 \text {; }
$$

$$
\left\{\text { result }=x_{0}^{n_{0}}\right\}
$$

\}else \{

$$
\left\{n \neq 0 \wedge x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}
$$

if $(\mathrm{n} \% 2==0)$ \{
$\left\{n \bmod 2=0 \wedge n \neq 0 \wedge x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}$
$\Longrightarrow\left\{x \cdot x>0 \wedge n / 2 \geq 0 \wedge x \cdot x=x_{0} \cdot x_{0} \wedge n / 2=n_{0} / 2 \wedge n_{0} \bmod 2=0\right\} \quad$ (inv-rule)
$\Downarrow\left\{x \cdot x>0 \wedge n / 2 \geq 0 \wedge x \cdot x=x_{0} \cdot x_{0} \wedge n / 2=n_{0} / 2\right\}$ (subst-rule)
$\Downarrow\left\{x \cdot x>0 \wedge n / 2 \geq 0 \wedge x \cdot x=x_{0}^{\prime} \wedge n / 2=n_{0}^{\prime}\right\}$ (subst-rule)
$\left\{x \cdot x>0 \wedge n / 2 \geq 0 \wedge x \cdot x=x_{0} \wedge n / 2=n_{0}\right\}$
$\Downarrow\left\{\right.$ this $\neq$ null $\left.\wedge x \cdot x>0 \wedge n / 2 \geq 0 \wedge x \cdot x=x_{0} \wedge n / 2=n_{0}\right\} \quad$ (invocation-rule)
result $=$ this.pow (x*x, $n / 2$ );
$\Uparrow\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$ (invocation-rule)
$\Uparrow\left\{\right.$ result $\left.=x_{0}^{\prime n_{0}^{\prime}}\right\}$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=\left(x_{0} \cdot x_{0}\right)^{n_{0} / 2}\right\} \quad$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=\left(x_{0} \cdot x_{0}\right)^{n_{0} / 2} \wedge n_{0} \bmod 2=0\right\} \quad$ (inv-rule)
\{result $\left.=x_{0}{ }^{n_{0}}\right\}$
\}else\{
$\left\{n \bmod 2 \neq 0 \wedge n \neq 0 \wedge x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}$
$\Longrightarrow$
$\left\{n>0 \wedge x>0 \wedge x=x_{0} \wedge n=n_{0}\right\}$
$\Longrightarrow$
$\Downarrow\left\{x>0 \wedge n-1 \geq 0 \wedge x=x_{0} \wedge n-1=n_{0}-1 \wedge x=x_{0}\right\}$ (invocation-var-rule)
$\Downarrow\left\{x>0 \wedge n-1 \geq 0 \wedge x=x_{0} \wedge n-1=n_{0}-1 \wedge x_{f}=x_{0}\right\} \quad$ (inv-rule)
$\Downarrow\left\{x>0 \wedge n-1 \geq 0 \wedge x=x_{0} \wedge n-1=n_{0}-1\right\}$ (subst-rule)
$\Downarrow\left\{x>0 \wedge n-1 \geq 0 \wedge x=x_{0}^{\prime} \wedge n-1=n_{0}^{\prime}\right\}$ (subst-rule) $\left\{x>0 \wedge n-1 \geq 0 \wedge x=x_{0} \wedge n-1=n_{0}\right\}$
$\Longrightarrow$
$\Downarrow\left\{\right.$ this $\neq$ null $\left.\wedge x>0 \wedge n-1 \geq 0 \wedge x=x_{0} \wedge n-1=n_{0}\right\} \quad$ (invocation-rule)
result $=$ this. pow (x, $\mathrm{n}-1$ );
$\Uparrow\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$ (invocation-rule)
$\Uparrow\left\{\right.$ result $\left.=x_{0}^{\prime n_{0}^{\prime}}\right\}$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=x_{0}{ }^{n_{0}-1}\right\} \quad$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=x_{0}{ }^{n_{0}-1} \wedge x_{f}=x_{0}\right\} \quad$ (inv-rule)
\{result $\left.\cdot x_{f}=x_{0}{ }^{n_{0}} \wedge x_{f}=x_{0}\right\}$
$\Uparrow\left\{\right.$ result $\left.\cdot x=x_{0}{ }^{n_{0}} \wedge x=x_{0}\right\}$ (invocation-var-rule)
$\Longrightarrow$
$\Uparrow\left\{\right.$ result $\left.\cdot x=x_{0}{ }^{n_{0}}\right\}$
$\left\{\right.$ result $\left.\cdot x=x_{0}^{n_{0}}\right\}$
result $=$ result*x;
$\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$
\}
$\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$

$$
\}
$$

$\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$
Listing 3: Verification of the recursive calculation of $x^{n}$ : Continuing section 3.2 .1 on page 15 , we prove the Java-K implementation to allow comparison with the $\mathrm{VM}_{\mathrm{K}}$ proof that follows.

### 3.5.2 $\quad \mathrm{VM}_{\mathrm{K}}$

Our assumption

$$
\left\{P \equiv x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\} \text { Recursiv : pow }\left\{Q \equiv \text { result }=x_{0}^{n_{0}}\right\}
$$

cannot be used directly for the recursive method invocations. We have to deduce two other triples that are more useful in our calling contexts. As for Java-K, we use a linear notation here that makes it easier to grasp the idea of the proof. Note that in a fully formal presentation, the proof would consist of a list of derivation trees for individual instructions. We could then integrate the modifications we make to our original assumption into a derivation tree of an instruction (the invokevirtual instruction). See section 5.3 on page 44 for a fully formal bytecode proof that has been automatically generated from a source proof.

- $\Downarrow\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \cdot x_{0} \wedge n=n_{0} / 2 \wedge n_{0} \bmod 2=0\right\} \quad$ (inv-rule)
$\Downarrow\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \cdot x_{0} \wedge n=n_{0} / 2\right\}$ (subst-rule)
$\Downarrow\left\{x>0 \wedge n \geq 0 \wedge x=x_{0}^{\prime} \wedge n=n_{0}^{\prime}\right\} \quad$ (subst-rule)
$\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}$
Recursiv: pow
$\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$
$\Uparrow\left\{\right.$ result $\left.=x_{0}^{\prime n_{0}^{\prime}}\right\}$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=\left(x_{0} \cdot x_{0}\right)^{\left(n_{0} / 2\right)}\right\}$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=\left(x_{0} \cdot x_{0}\right)^{\left(n_{0} / 2\right)} \wedge n_{0} \bmod 2=0\right\} \quad($ inv-rule $)$
- $\Downarrow\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}-1 \wedge x_{f}=x_{0}\right\}$ (inv-rule)
$\Downarrow\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}-1\right\}$ (subst-rule)
$\Downarrow\left\{x>0 \wedge n \geq 0 \wedge x=x_{0}^{\prime} \wedge n=n_{0}^{\prime}\right\} \quad$ (subst-rule)
$\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}$
Recursiv: pow
$\left\{\right.$ result $\left.=x_{0}^{n_{0}}\right\}$
$\Uparrow\left\{\right.$ result $\left.=x_{0}^{\prime n_{0}^{\prime}}\right\}$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=x_{0}{ }^{n_{0}-1}\right\} \quad$ (subst-rule)
$\Uparrow\left\{\right.$ result $\left.=x_{0}{ }^{n_{0}-1} \wedge x_{f}=x_{0}\right\} \quad$ (inv-rule)
$\rightarrow$ \{result $\left.\cdot x_{f}=x_{0}{ }^{n_{0}} \wedge x_{f}=x_{0}\right\}$
test-1: pushvar n
$\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0} \wedge(s(0) \neq 0)=(n \neq 0)\right\}$
unop $<>0$,
$\left\{x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0} \wedge s(0)=(n \neq 0)\right\}$
brtrue test-2
path-1: $\quad\left\{n=0 \wedge x>0 \wedge n \geq 0 \wedge x=x_{0} \wedge n=n_{0}\right\}$

```
pushc 1
{s(0)=\mp@subsup{x}{0}{\mp@subsup{n}{0}{}}}
pop result
{result = 和䅐}
goto end
test-2: {n\not=0^x>0\wedgen\geq0\wedgex= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
pushvar n
{((s(0) mod 2) f=0)=(n\operatorname{mod}2\not=0)\wedgen\not=0^x>0\wedgen\geq0\wedgex= \mp@subsup{x}{0}{}\wedgen= n n }
pushc 2
{((s(1) mod s(0))\not=0)=(n\operatorname{mod}2\not=0)\wedgen\not=0\wedgex>0\wedgen\geq0\wedgex= \mp@subsup{x}{0}{}\wedgen= n ( 
binop "%"
{(s(0)\not=0)=(n\operatorname{mod}2\not=0)\wedgen\not=0\wedgex>0\wedgen\geq0\wedgex= \mp@subsup{x}{0}{}\wedgen= no n
unop <>0
{s(0)=(n\operatorname{mod}2\not=0)\wedgen\not=0\wedgex>0\wedgen\geq0\wedge x= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
brtrue path-3
path-2: {n mod 2=0^n\not=0^x>0\wedgen\geq0\wedgex= x \ \ n = nov
pushvar this
{n mod 2=0^n\not=0^x>0^n\geq0^x= x 0^n= no}
pushvar x
```



```
pushvar x
{s(1)\cdots(0)=x\cdotx\wedge\wedgen\operatorname{mod}2=0\wedgen>0\wedgex>0\wedgex= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
binop "*"
{s(0)=x\cdotx\wedgen mod 2=0^n>0^x>0^x= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
pushvar n
{(s(0)/2) = n/2\wedges(1)=x\cdotx\wedgen\operatorname{mod}2=0\wedgen>0\wedgex>0\wedgex= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
pushc 2
```



```
binop "/"
{s(0) = n/2\wedges(1) = x x ^ n mod 2=0^n>0\wedgex>0\wedge ( 
```



```
invokevirtual Recursiv:pow
{(result }=(\mp@subsup{x}{0}{}\cdot\mp@subsup{x}{0}{}\mp@subsup{)}{}{(\mp@subsup{n}{0}{}/2)}\wedge\mp@subsup{n}{0}{}\operatorname{mod}2=0)[s(0)/\mathrm{ result }]
->{s(0)=\mp@subsup{x}{0}{\mp@subsup{n}{0}{}}}
pop result
{result = 和 䅐}
goto end
path-3: {n\operatorname{mod}2\not=0\wedgen>0\wedgex>0\wedgex= 和\wedgen= nov
pushvar x
{s(0)=x\wedgex>0\wedgen>0\wedgex= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
pushvar this
{x=x\wedges(1)=x\wedgen>0\wedgex>0\wedge x= 秋\wedgen= not}
pushvar x
{n-1=n-1\wedges(0)=x\wedges(2)=x\wedgen>0\wedgex>0\wedgex= \mp@subsup{x}{0}{}\wedgen= n= no}
pushvar n
{(s(0)-1)=n-1\wedges(1)=x\wedges(3)=x\wedgen>0\wedge x>0^x= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
pushc 1
{(s(1)-s(0))=n-1\wedges(2)=x\wedges(4)=x\wedgen>0\wedgex>0\wedgex= \mp@subsup{x}{0}{}\wedgen=\mp@subsup{n}{0}{}}
binop "-"
{s(0)=n-1\wedges(1)=x\wedges(3)=x\wedgen>0^x>0\wedgex= \mp@subsup{x}{0}{}\wedgen=n=\mp@subsup{n}{0}{}}
\Downarrow{s(1)>0\wedges(0)\geq0\wedges(1)=\mp@subsup{x}{0}{}\wedges(0)=\mp@subsup{n}{0}{}-1\wedges(3)=\mp@subsup{x}{0}{}}\mathrm{ (invokevar)}
```



```
invokevirtual Recursiv:pow
->{(result }\cdot\mp@subsup{x}{f}{}=\mp@subsup{x}{0}{}\mp@subsup{}{}{\mp@subsup{n}{0}{\prime}}\wedge\mp@subsup{x}{f}{}=\mp@subsup{x}{0}{})[s(0)/\mathrm{ result }]
\Uparrow{s(0)\cdots(1)=\mp@subsup{x}{0}{}\mp@subsup{}{}{\mp@subsup{n}{0}{\prime}}\wedges(1)=\mp@subsup{x}{0}{}}\mathrm{ (invokevar)}
->{s(1)\cdots(0)=\mp@subsup{x}{0}{\mp@subsup{n}{0}{}}}
binop "*"
```

$$
\begin{aligned}
& \left\{s(0)=x_{0}^{n_{0}}\right\} \\
& \text { pop result } \\
& \left\{\text { result }=x_{0}^{n_{0}}\right\} \\
& \text { end_method }
\end{aligned}
$$

Listing 4: Verification of the recursive calculation of $x^{n}$ : We prove the $\mathrm{VM}_{\mathrm{K}}$ implementation of the example in section 3.2.2 on page 15. The proof should give a rough idea how the programming logic for bytecode can be used in practice to prove method implementations correct.

## 4 Proof transformation from Source to Bytecode

This section presents the procedure for translating proofs for method specifications from Java-K to $\mathrm{VM}_{\mathrm{K}}$ :

$$
\frac{\{\text { this } \neq \operatorname{null} \wedge P\} \operatorname{body}(T @ m)\{Q\}}{\{P\} T @ m\{Q\}} \Leftrightarrow \mathrm{VM}_{\mathrm{K}} \frac{\ldots}{\{P\} T @ m\{Q\}}
$$

Before we can do so, we have to fix some conventions and to make a few observations that help construct the bytecode proofs. It will be useful to have an additional notion for expressions that do not contain program variables:

Definition 11. Expressions that do not contain program variables are called $\Sigma$-terms.

### 4.1 Simplifications for the Translation

The translation of proofs presented here is simple. Various improvements are possible. Two important conventions can not be seen from the pattern-based translation rules.

The set of local variables and parameters is not changed during translation. No variables are eliminated, no new variables are introduced. Elimination of local variables that are dead at certain points in the analyzed region is an optimization that would either necessitate a two step translation (compilation and then optimization) or would render our formalism more complicated. Similarly for assertions: we assume that trivial proof transformation for assertions is possible: a source proof an assertion $P$ can be used as the proof of the translated assertion $P$ on the bytecode level. Since we do not translate program variables and we do not change logical variables, we do not need to translate assertions. It is sometimes necessary for the correctness of the generated proofs to keep in mind that assertions in the source proof do not contain references to the stack $s$.

### 4.2 Form of the Translation Patterns

We use three compilation function for the translation of source proofs:
$\nabla^{S}$ for the translation of proof trees that prove statements
$\nabla^{\mathrm{E}}$ for the translation of expressions to simplify the notation used in the definition of $\nabla^{\mathrm{S}}$ (the $\nabla^{\mathrm{E}}$ function has weaker requirements on its input parameters than $\nabla^{\mathrm{X}}$ )
$\nabla^{\mathrm{X}}$ for the translation of expressions

These functions return sequences of specified instructions that fulfill the basic block property. If control flow enters the sequence at the beginning of the first statement, then control flow will leave the sequence at the end of the last statement. $\nabla^{\mathrm{S}}, \nabla^{\mathrm{E}}$ and $\nabla^{\mathrm{X}}$ are defined depending on the shape of their arguments, i.e., the translation is pattern based.

The compilation function for statements $\nabla^{S}$ takes proof trees as its argument and its definition depends only on the rule that is used at the root, i.e., $\nabla^{S}$ can handle any valid Java-K derivation tree ${ }^{9}$ and always returns a sequence whose pre- and postconditions are the same as the pre- and post-conditions of the the root of the argument derivation tree.

Both $\nabla^{\mathrm{X}}$ and $\nabla^{\mathrm{S}}$ take three arguments $(P, e, Q)$ : they produce a sequence of instructions calculating e on top of the stack where $P$ is the precondition of the sequence and $Q$ is its postcondition. Unlike $\nabla^{\mathrm{S}}$, the translation of expression is defined only for special cases of $P$ and $Q$ : the shape of the pre- and post-conditions is constrained, $\nabla^{\mathrm{X}}$ and $\nabla^{\mathrm{E}}$ are used only with arguments that obey these constraints.

We abbreviate the instantiation of $\nabla^{S}$. We write " $\nabla^{S}$ (consequent)" and mean
" $\nabla^{\mathrm{S}}$ (the whole tree that leads to the consequent)" when it is clear from the context what the proof looks like:

$$
\nabla^{\mathrm{S}}(\{P\} S\{Q\}) \equiv \nabla^{\mathrm{S}}\left(\operatorname{rule} \frac{\cdots\{P\} S\{Q\} \cdots}{\left\{P^{\prime}\right\} S\left\{Q^{\prime}\right\}}\right)
$$

The proof for the method body is assembled from small pieces that are concatenated to form larger pieces that form the whole $\mathrm{VM}_{\mathrm{K}}$ proof for the whole method body.

Example 5. Sequential composition is the easiest rule to translate

$$
\begin{aligned}
\nabla^{\mathrm{S}}\left(\operatorname{seq} \frac{\{P\} S_{1}\{Q\} \quad\{Q\} S_{2}\{R\}}{\{P\} S_{1} ; S_{2}\{R\}}\right)= & \\
& S_{1}:\{P\} \\
& \nabla^{\mathrm{S}}\left(\{P\} S_{1}\{Q\}\right) \\
& S_{2}:\{Q\} \\
& \nabla^{\mathrm{S}}\left(\{Q\} S_{2}\{R\}\right) \\
& \{R\}
\end{aligned}
$$

The rule is to be read as follows:
if the root of the derivation tree is an application of the "seq"-rule, the the proof tree must have the form

$$
\operatorname{seq} \frac{\frac{T_{1}}{\{P\} S_{1}\{Q\}}}{\{P\} S_{1} ; S_{2}\{R\}} \frac{T_{1}}{\{Q\} S_{2}\{R\}}
$$

which we abbreviate as

$$
\operatorname{seq} \frac{\{P\} S_{1}\{Q\} \quad\{Q\} S_{2}\{R\}}{\{P\} S_{1} ; S_{2}\{R\}}
$$

[^5]then the translation of this tree is the concatenation of the two verified instruction sequences obtained from $p_{1} \equiv \nabla^{\mathrm{S}}\left(\{P\} S_{1}\{Q\}\right)$ and $p_{2} \equiv \nabla^{\mathrm{S}}\left(\{Q\} S_{2}\{R\}\right)$ where the triple $\{P\} S_{1}\{Q\}$ again stands for the whole tree that leads to this conclusion: $\frac{T_{1}}{\{P\} S_{1}\{Q\}}$. Note that the well-formedness condition
\[

$$
\begin{aligned}
\operatorname{precondition}_{\mathfrak{p}}(l) & =E_{l} \\
\operatorname{postcondition}_{\mathfrak{p}}(l) & =E_{l+1}=\operatorname{precondition}_{\mathfrak{p}}(l+1)
\end{aligned}
$$
\]

is respected at the boundary between the two sequences. " $S_{1}$ :" and " $S_{2}$ :" are symbolic labels. They stand for the actual labels at the beginning of the sequences $p_{1}$ and $p_{2}$ resp. Logical labels are also introduced for newly introduced instructions (e.g., $A$ : pop $x$ in the translation of the assign rules). These are always fresh labels that stand for the actual numerical label within the definition.
Example 6. Consider the translation rule for the weak-rule: $\nabla^{\mathrm{S}}\left(\right.$ weak $\left.\frac{Q \rightarrow Q^{\prime} \quad\{P\} S\{Q\}}{\{P\} S\left\{Q^{\prime}\right\}}\right)=$

$$
\begin{aligned}
& L:\{P\} \\
& \quad \nabla^{\mathrm{S}}(\{P\} S\{Q\}) \\
& P:\{Q\} \\
& \quad \operatorname{nop} \\
& \quad\left\{Q^{\prime}\right\}
\end{aligned}
$$

We're introducing a new instruction nop into our result sequence of verified instruction. The problem is that nop is not verified itself. According to the nop-rule

$$
\operatorname{nop} \frac{E_{l} \rightarrow E_{l+1}}{\left\{E_{l}\right\} l: \operatorname{nop}\left\{E_{l+1}\right\}}
$$

We have to prove $E_{l} \rightarrow E_{l+1}$ in order to make the generated nop a verified instruction as required for the proof of a method implementation.

This is what the sections labeled "Reasons" are here for: they contain proofs for the antecedents of the $\mathrm{VM}_{\mathrm{K}}$ rules that are used in the translation. They have to be adapted to a specific deductive system for the underlying logic to be mechanically checkable.

### 4.3 Making the Translation Easier to Understand

We sometimes argue about all rules of the $\mathrm{VM}_{\mathrm{K}}$ logic by referring to two functions $f, g$ that we claim to be distributive with respect to the boolean operations $\wedge, \vee$ and commutative with respect to substitution of logical variables by $\Sigma$-terms. This is to be understood as follows.

Looking at section 3.4 on page 16 , we see that "most", the ordinary rules, with the notable exceptions of invokevirtual and invokevar, have a special and simple form. Their antecedents consist of a logical implication " $\rightarrow$ " between two program assertions on which we apply a function consisting of substitutions, shifts and adding one simple conjunction.

Example 7. The pushc rule

$$
\operatorname{pushc} \frac{\operatorname{shift}\left(E_{l}\right) \rightarrow E_{l+1}[v / s(0)]}{\left\{E_{l}\right\} l: \text { pushc } v\left\{E_{l+1}\right\}}
$$

has an antecedent of the form

$$
f\left(E_{l}\right) \rightarrow g\left(E_{l+1}\right)
$$

where $f(E)=\operatorname{shift}(E)$ and $g(E)=E[v / s(0)]$

For the ordinary instructions has antecedents $f\left(E_{l^{\prime}}\right) \rightarrow g\left(E_{l^{\prime \prime}}\right)$ where $f$ and $g$ have the form

$$
E \mapsto \operatorname{shift}^{k}(E)[\cdots] \wedge A
$$

For a specific function $f$ :

$$
f(E)=\underbrace{\operatorname{shift}^{k_{f}}(E)\left[R_{f}\right]}_{f^{-}(E)} \wedge A_{f}
$$

It is now easy to check that the ordinary rules follow this pattern: ( $v$ ranges over values, $x$ over local variables)

$$
\begin{aligned}
f, g \in\{ & \operatorname{shift}(.), \\
& (.)[v / s(0)] \\
& (.)[x / s(0)] \\
& (.), \\
& (\operatorname{shift}(.))[s(0) / x], \\
& (.)[(\operatorname{op} s(0)) / s(0)], \\
& (\operatorname{shift}(.))[(s(1) \text { op } s(0)) / s(1)], \\
& (.) \wedge \neg s(0), \\
& \operatorname{shift}(.), \\
& (.) \wedge s(0), \\
& (.) \wedge \tau(s(0)) \preceq T, \\
& (.)[\operatorname{new}(\$, T) / s(0), \$\langle T\rangle / \$], \\
& (.)[\$(\operatorname{iv}(s(0), S @ a)) / s(0)] \wedge s(0) \neq \text { null, } \\
& \left(\operatorname{shift}{ }^{2}(.)\right)[\$\langle\mathrm{iv}(s(1), S @ a):=s(0)\rangle / \$] \wedge s(1) \neq \text { null } \\
& \}
\end{aligned}
$$

And that these functions are indeed distributive w.r.t the boolean operators $\wedge, \vee$.

$$
\begin{aligned}
& \left(\operatorname{shift}^{k}\left(E_{1}\right)[\cdots] \wedge A\right) \wedge\left(\operatorname{shift}^{k}\left(E_{2}\right)[\cdots] \wedge A\right)=\left(\operatorname{shift}^{k}\left(E_{1} \wedge E_{2}\right)[\cdots] \wedge A\right) \\
& \left(\operatorname{shift}^{k}\left(E_{1}\right)[\cdots] \wedge A\right) \vee\left(\operatorname{shift}^{k}\left(E_{2}\right)[\cdots] \wedge A\right)=\left(\operatorname{shift}^{k}\left(E_{1} \vee E_{2}\right)[\cdots] \wedge A\right)
\end{aligned}
$$

invokevirtual and invokevar are less regular, their antecedents' form is $E_{l} \rightarrow X \wedge Y \rightarrow E_{l+1}$

## 4.4 nops Generated During Translation

nop instructions may be generated during translation. They can removed in a second pass (through the $\mathrm{VM}_{\mathrm{K}}$ proof) if needed: all rules have the $f, g$ form explained above. Both before and after an instruction, we replace nop's using the knowledge, that the implication is transitive either

1. "after" ${ }^{10}$ another instruction if $g$ is the identity (goto, invokevirtual, invokevar, nop, method)

$$
(f(A) \rightarrow B \wedge B \rightarrow C) \rightarrow(f(A) \rightarrow C)
$$

or
${ }^{10}$ all successor instructions
2. before another instruction if $f$ is the identity (pop, unop, binop, goto, checkcast, getfield, putfield, onvokevirtual, invokevar, nop, method):

$$
(A \rightarrow B \wedge B \rightarrow g(C)) \rightarrow(A \rightarrow g(C))
$$

Example 8. Consider the following sequence from section 5.3 on page 44

$$
\left[\frac{T_{1}}{\frac{\mathrm{x}=x_{0} \rightarrow\left(\mathrm{x}=x_{0} \wedge 1=1\right)}{\left\{\mathrm{x}=x_{0}\right\} 1: \operatorname{nop}\left\{\left(\mathrm{x}=x_{0} \wedge 1=1\right)\right\}}}, \frac{T_{2}}{\left\{\left(\mathrm{x}=x_{0} \wedge 1=1\right)\right\} 2: \operatorname{pushc} 1\left\{\left(\mathrm{x}=x_{0} \wedge 1=1\right)\right) \rightarrow\left(\mathrm{x}=x_{0} \wedge 1=s(0)\right)[1 / s(0)]}\right]
$$

In this context, it seems that nops cannot be removed: Rule 1 is not applicable because the nop has no predecessor and rule 2 is not applicable either (because shift(.) is not the identity). This is not a problem however. shift does only rename free variables, which does not change the truth-value of a formula:

Taking this special case into account, we can apply the extended rule 2 to pushvar and pushc as well. This is important: nop before pushvar and pushc are generated for every expression that is translated by $\nabla^{\mathrm{E}}$.

Closer examinations of the actual proof-sequences that can be produced show that all nops can be removed from the proofs we generate by just applying the two rules above when appropriate.

### 4.5 Limitation: $\exists$ Elimination and $\forall$ Introduction

We are convinced that the ex- and the all-rules are not strictly necessary on the bytecode level. We believe that they can always be lifted to the outmost level of method body proofs an then pulled down into the proof for the method identifier $(T @ m)$. Unfortunately, we do know how this is achieved, in general. For now we just assume that ex- and all-rules do not occur in source proofs for method bodies. Thus source proofs should have a special normal form.

Other possibilities of handling the ex- and all-rules are more invasive: they may make the mechanical verification of a proof cumbersome or destroy other desirable properties of our current proof rules like having exactly one rule for every instruction ${ }^{11}$.

[^6]
### 4.6 Expressions

### 4.6.1 How Expressions are Translated

The translation of expressions is special: expressions are atomic entities for source logic, but not on for the $\mathrm{VM}_{\mathrm{K}}$.
$\nabla^{\mathrm{E}}(P, e, Q \equiv \operatorname{shift} P \wedge e=s(0))=$

$$
\begin{aligned}
L & : \\
& \{P\} \\
& \text { nop } \\
E: & \left\{P^{\prime} \equiv P \wedge e=e\right\} \\
& \nabla^{\mathrm{x}}\left(P^{\prime}, e, Q\right) \\
& \{Q\}
\end{aligned}
$$

## Reasons

$L$ : nop $\quad P \rightarrow P \wedge e=e$ is trivially true.

### 4.6.2 $e_{1}$ op $e_{2}$

This and the next two sections do the actual work for translating expressions.

$$
\begin{gathered}
\nabla^{\mathrm{X}}\left(Q_{s} \equiv Q \wedge \text { unshift }(P[e / s(0)]), e \equiv\left(e_{1} \text { op } e_{2}\right), Q_{e} \equiv \operatorname{shift}(Q) \wedge P\right)= \\
E_{1}:\left\{Q_{s}\right\} \\
E_{2}:\left\{Q_{2}\right\} \\
\left(Q_{s}, e_{1}, Q_{2} \equiv \operatorname{shift}(Q) \wedge P\left[\left(s(0) \text { op } e_{2}\right) / s(0)\right]\right) \\
P:\left\{Q_{3}\right\} \\
\operatorname{binop}_{\mathrm{op}} \\
\\
\left\{Q_{e}\right\}
\end{gathered}
$$

Reasons: binop $_{\text {op }}$

$$
\begin{aligned}
& Q_{3} \rightarrow\left(\operatorname{shift}\left(Q_{e}\right)\right)[(s(1) \text { op } s(0)) / s(1)] \\
= & \operatorname{shift}^{2}(Q) \wedge(\operatorname{shift}(P))[(s(1) \text { op } s(0)) / s(1)] \rightarrow(\operatorname{shift}(\operatorname{shift}(Q) \wedge P))[(s(1) \text { op } s(0)) / s(1)] \\
= & \operatorname{shift}^{2}(Q) \wedge(\operatorname{shift}(P))[(s(1) \text { op } s(0)) / s(1)] \\
& \rightarrow \operatorname{shift}^{2}(Q)[(s(1) \text { op } s(0)) / s(1)] \wedge(\operatorname{shift}(P))[(s(1) \text { op } s(0)) / s(1)] \\
= & \operatorname{shift}^{2}(Q) \wedge(\operatorname{shift}(P))[(s(1) \text { op } s(0)) / s(1)] \rightarrow \operatorname{shift}^{2}(Q) \wedge(\operatorname{shift}(P))[(s(1) \text { op } s(0)) / s(1)]
\end{aligned}
$$

### 4.6.3 Constants

$\nabla^{\mathrm{X}}\left(Q_{s} \equiv Q \wedge \operatorname{unshift}(P[e / s(0)]), e \equiv c, Q_{e} \equiv \operatorname{shift}(Q) \wedge P\right)=$

$$
L:\left\{Q_{s}\right\}
$$

pushc $c$
$\left\{Q_{e}\right\}$

Reasons: pushc $c$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \rightarrow Q_{e}[c / s(0)] \\
&=\operatorname{shift}(Q \wedge \operatorname{unshift}(P[e / s(0)])) \rightarrow(\operatorname{shift}(Q) \wedge P))[c / s(0)] \\
&= \operatorname{shift}(Q) \wedge \operatorname{shift}(u n s h i f t \\
&=\operatorname{shift}(Q) \wedge P[e / s(0)])) \rightarrow \operatorname{shift}(Q)[c / s(0)] \wedge P[c / s(0)] \\
&= \operatorname{shift}(Q) \wedge P[e / s(0)] \rightarrow \operatorname{shift}(Q)[c / s(0)] \wedge P[c / s(0)] \\
& \operatorname{shift}(Q) \wedge P[c / s(0)]
\end{aligned}
$$

### 4.6.4 Variables

$\nabla^{\mathrm{X}}\left(Q_{s} \equiv Q \wedge \operatorname{unshift}(P[e / s(0)]), e \equiv x, Q_{e} \equiv \operatorname{shift}(Q) \wedge P\right)=$
$L:\left\{Q_{s}\right\}$
pushvar $x$
$\left\{Q_{e}\right\}$

Reasons: pushvar $x$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \rightarrow Q_{e}[x / s(0)] \\
= & \operatorname{shift}(Q \wedge \operatorname{unshift}(P[e / s(0)])) \rightarrow(\operatorname{shift}(Q) \wedge P))[x / s(0)] \\
= & \operatorname{shift}(Q) \wedge \operatorname{shift}(\operatorname{unshift}(P[e / s(0)])) \rightarrow \operatorname{shift}(Q)[x / s(0)] \wedge P[x / s(0)] \\
= & \operatorname{shift}(Q) \wedge P[e / s(0)] \rightarrow \operatorname{shift}(Q)[x / s(0)] \wedge P[x / s(0)] \\
= & \operatorname{shift}(Q) \wedge P[e / s(0)] \rightarrow \operatorname{shift}(Q) \wedge P[x / s(0)]
\end{aligned}
$$

### 4.6.5 Example for Expressions

The evaluation of $\nabla^{\mathrm{E}}(P, x \cdot y, Q \equiv P \wedge(x \cdot y)=s(0))$ yields the partial proof

1. $\frac{(P \rightarrow(P \wedge(x \cdot y)=(\mathrm{x} \cdot \mathrm{y})))}{\{P\} \text { 1 }: \operatorname{nop}\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y}))\}}$
2. $\frac{((\operatorname{shift}(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y}))) \rightarrow((\text { shift } P) \wedge(\mathrm{x} \cdot \mathrm{y})=s(0)[(s(0) \cdot \mathrm{y}) / s(0)])[\mathrm{x} / s(0)])}{\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y}))\} 2: \text { pushvar } \mathrm{x}\{((\operatorname{shift} P) \wedge(\mathrm{x} \cdot \mathrm{y})=s(0)[(s(0) \cdot \mathrm{y}) / s(0)])\}}$
3. $\frac{((\operatorname{shift}((\operatorname{shift} P) \wedge(\mathrm{x} \cdot \mathrm{y})=s(0)[(s(0) \cdot \mathrm{y}) / s(0)])) \rightarrow((\operatorname{shift}(\operatorname{shift} P)) \wedge(\operatorname{shift}(\mathrm{x} \cdot \mathrm{y})=s(0))[(s(1) \cdot s(0)) / s(1)])[\mathrm{y} / s(0)])}{\{((\operatorname{shift} P) \wedge(\mathrm{x} \cdot \mathrm{y})=s(0)[(s(0) \cdot \mathrm{y}) / s(0)])\} \text { Pushvar y }\{((\operatorname{shift}(\operatorname{shift} P)) \wedge(\operatorname{shift}(\mathrm{x} \cdot \mathrm{y})=s(0))[(s(1) \cdot s(0)) / s(1)])\}}$
4. $\frac{(((\operatorname{shift}(\operatorname{shift} P)) \wedge(\operatorname{shift}(\mathrm{x} \cdot \mathrm{y})=s(0))[(s(1) \cdot s(0)) / s(1)]) \rightarrow(\operatorname{shift}((\operatorname{shift} P) \wedge(\mathrm{x} \cdot \mathrm{y})=s(0)))[(s(1) \cdot s(0)) / s(1)])}{\{((\operatorname{shift}(\operatorname{shift} P)) \wedge(\operatorname{shift}(\mathrm{x} \cdot \mathrm{y})=s(0))[(s(1) \cdot s(0)) / s(1)])\} 4: \text { binop} \cdot\{((\operatorname{shift} P) \wedge(\mathrm{x} \cdot \mathrm{y})=s(0))\}}$
or, after some simplifications and assuming that $P$ is a source proof assertion:
5. $\frac{(P \rightarrow(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y})))}{\{P\} 1: \operatorname{nop}\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y}))\}}$
6. $\frac{((P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y})) \rightarrow(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y})))}{\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(\mathrm{x} \cdot \mathrm{y}))\} 2: \text { pushvar } \mathrm{x}\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(0) \cdot \mathrm{y}))\}}$
7. $\frac{((P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(1) \cdot \mathrm{y})) \rightarrow(P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(1) \cdot \mathrm{y})))}{\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(0) \cdot \mathrm{y}))\} 3: \text { pushvar } \mathrm{y}\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(1) \cdot s(0)))\}}$
8. $\frac{((P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(1) \cdot s(0))) \rightarrow(P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(1) \cdot s(0))))}{\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=(s(1) \cdot s(0)))\} 4: \text { binop } \cdot\{(P \wedge(\mathrm{x} \cdot \mathrm{y})=s(0))\}}$

### 4.7 Base Rules

### 4.7.1 assign

$$
\begin{aligned}
& \nabla^{\mathrm{S}}\left(\operatorname{assign} \frac{}{\{P[e / x]\} x=e\{P\}}\right)= \\
& V:\{P[e / x]\} \\
& \nabla^{\mathrm{E}}(P[e / x], e, \operatorname{shift}(P[e / x]) \wedge s(0)=e) \\
& A:\{\operatorname{shift}(P[e / x]) \wedge s(0)=e\} \\
& \operatorname{pop} x \\
&\{P\}
\end{aligned}
$$

## Reasons

$A$ : pop $x$

$$
\begin{aligned}
& \text { P is a source assertion, it does not contain references to the stack } \\
& \underbrace{\operatorname{shift}(P[e / x])}_{P[e / x]} \wedge s(0)=e \rightarrow(\underbrace{\operatorname{shift}(P)}_{P})[s(0) / x] \\
& =P[s(0) / x] \wedge s(0)=e \rightarrow P[s(0) / x]
\end{aligned}
$$

### 4.7.2 while

$\nabla^{\mathrm{S}}\left(\right.$ while $\left.\frac{\{e=\top \wedge P\} S\{P\}}{\{P\} \text { while }(e)\{S\}\{e=\perp \wedge P\}}\right)=$

$$
\begin{aligned}
L: & \{P\} \\
& \text { goto } T \\
S: & \{e=\top \wedge P\} \\
& \nabla^{\mathrm{S}}(\{e=\top \wedge P\} S\{P\}) \\
T: & \{P\} \\
& \nabla^{\mathrm{E}}(P, e, \operatorname{shift}(P) \wedge s(0)=e) \\
B: & \{\operatorname{shift}(P) \wedge s(0)=e\} \\
& \operatorname{brtrue} S \\
& \{P \wedge e=\perp\}
\end{aligned}
$$

## Reasons

$L$ : goto $T$ We have to prove: $P \rightarrow P$, trivial
$B$ : brtrue $S$ We have to prove:

$$
\operatorname{shift}(P) \wedge s(0)=e \wedge \neg s(0) \rightarrow \operatorname{shift}(P \wedge e=\perp)
$$

trivial, and

$$
\operatorname{shift}(P) \wedge s(0)=e \wedge s(0) \rightarrow \operatorname{shift}(e=\top \wedge P)
$$

also trivial

### 4.7.3 if

In the produced code, the else-part comes before the then-part of the statement.

$$
\begin{gathered}
\nabla^{\mathrm{S}}\left(\mathrm{if} \frac{\{e=\top \wedge P\} S_{1}\{Q\} \quad\{e=\perp \wedge P\} S_{2}\{Q\}}{\{P\} \operatorname{if}(e)\left\{S_{1}\right\} \mathrm{else}\left\{S_{2}\right\}\{Q\}}\right)= \\
T:\{P\} \\
B: \nabla^{\mathrm{E}}(P, e, \operatorname{shift}(P) \wedge s(0)=e) \\
\operatorname{brift}(P) \wedge s(0)=e\} \\
S_{2}:\{e=\perp \wedge P\} \\
\nabla^{\mathrm{S}}\left(\{e=\perp \wedge P\} S_{2}\{Q\}\right) \\
L:\{Q\} \\
\text { goto } E \\
S_{1}:\{e=\top \wedge P\} \\
\nabla^{\mathrm{S}}\left(\{e=\top \wedge P\} S_{1}\{Q\}\right) \\
E:\{Q\} \\
\text { nop } \\
\{Q\}
\end{gathered}
$$

## Reasons

$B$ : brtrue $S_{1}$ We have to prove:

$$
\operatorname{shift}(P) \wedge s(0)=e \wedge \neg s(0) \rightarrow \operatorname{shift}(e=\perp \wedge P)
$$

trivial, and

$$
\operatorname{shift}(P) \wedge s(0)=e \wedge s(0) \rightarrow \operatorname{shift}(e=\top \wedge P)
$$

also trivial

### 4.7.4 seq

$\nabla^{\mathrm{S}}\left(\operatorname{seq} \frac{\{P\} S_{1}\{Q\} \quad\{Q\} S_{2}\{R\}}{\{P\} S_{1} ; S_{2}\{R\}}\right)=$

$$
\begin{aligned}
& S_{1}: \\
&\{P\} \\
& \nabla^{\mathrm{S}}\left(\{P\} S_{1}\{Q\}\right) \\
& S_{2}:\{Q\} \\
& \nabla^{\mathrm{S}}\left(\{Q\} \quad S_{2} \quad\{R\}\right) \\
&\{R\}
\end{aligned}
$$

### 4.7.5 conjunct/disjunct

Conjunction and disjunction are treated identically. We show here only the conjunct-rule.
$\nabla^{\mathrm{S}}\left(\operatorname{conj} \frac{\left\{P_{1}\right\} S\left\{Q_{1}\right\} \quad\left\{P_{2}\right\} S\left\{Q_{2}\right\}}{\left\{P_{1} \wedge P_{2}\right\} S\left\{Q_{1} \wedge Q_{2}\right\}}\right)=$
create the two proofs

$$
\nabla^{S}\left(\left\{P_{1}\right\} S\left\{Q_{1}\right\}\right)
$$

and

$$
\nabla^{S}\left(\left\{P_{2}\right\} S\left\{Q_{2}\right\}\right)
$$

The embedded instructions are (by construction) the same. With the two proofs, we assemble a third proof (the result)

$$
\nabla^{\mathrm{S}}\left(\left\{P_{1} \wedge P_{2}\right\} S\left\{Q_{1} \wedge Q_{2}\right\}\right)
$$

by merging, for all instructions, their specifications

$$
\left\{A_{(l)}\right\} l: \operatorname{instr}\left\{A_{(l+1)}\right\}
$$

and

$$
\left\{B_{(l)}\right\} l: \operatorname{instr}\left\{B_{(l+1)}\right\}
$$

to get

$$
\left\{A_{(l)} \wedge B_{(l)}\right\} l: \text { instr }\left\{A_{(l+1)} \wedge B_{(l+1)}\right\}
$$

For the common instructions with that have the $f, g$ form (see section 4.3 on page 24), a proof tree can then be constructed easily:

This argument is not true for invokevirtual (i.e. for the invokevirtual and invokevar rules). In that case, we apply the the rule directly to the method specification that is required by invokevirtual if the two rules we're looking at are both invokevirtual:

$$
\begin{array}{ccc}
\frac{\ldots}{\left\{P_{1}\right\} T: m\left\{Q_{1}\right\}} & \frac{\cdots}{\left\{P_{2}\right\} T: m\left\{Q_{2}\right\}} \\
\left\{P_{1} \wedge P_{2}\right\} T: m\left\{Q_{1} \wedge Q_{2}\right\} & & \\
\left\{A_{(l)} \wedge B_{(l)}\right\} l: \text { invokevirtual } T: m\left\{A_{(l+1)} \wedge B_{(l+1)}\right\}
\end{array}
$$

In the case of at least one invokevar rule ${ }^{12}$, we propagate the conjunction up the proof tree. As a simplification, we assume that the logical variables used in the two proofs are disjoint (if this is not the case, we could always insert subst-rules)
given one tree

$$
\text { invokevar } \frac{\left\{A_{l}^{\prime}\right\} l: \text { invokevirtual } T: m\left\{A_{l+1}^{\prime}\right\} \quad A_{l} \rightarrow A_{l}^{\prime}[(\operatorname{shift}(w)) / Z] \quad A_{l+1}^{\prime}[w / Z] \rightarrow A_{l+1}}{\left\{A_{l}\right\} l: \text { invokevirtual } T: m\left\{A_{l+1}\right\}}
$$

and a second tree (represented by its root)

$$
\left\{B_{l}\right\} l: \text { invokevirtual } T: m\left\{B_{l+1}\right\}
$$

[^7]we produce
\[

$$
\begin{array}{cl}
\ldots & \begin{array}{l}
\left(A_{l} \wedge B_{l}\right) \rightarrow\left(A_{l}^{\prime} \wedge B_{l}\right)[(\operatorname{shift}(w)) / Z] \\
\text { invokevar } \frac{\left\{A_{l}^{\prime} \wedge B_{l}\right\} l: \text { invokevirtual } T: m\left\{A_{l+1}^{\prime} \wedge B_{l+1}\right\}}{} \\
\left\{A_{l} \wedge B_{l}\right\} l: \text { invokevirtual } T: m\left\{A_{l+1} \wedge B_{l+1}\right\}
\end{array}
\end{array}
$$
\]

where the rest of the tree (represented by dots) is obtained from merging the two trees

$$
\left\{A_{l}^{\prime}\right\} l: \text { invokevirtual } T: m\left\{A_{l+1}^{\prime}\right\}
$$

and

$$
\left\{B_{l}\right\} l: \text { invokevirtual } T: m\left\{B_{l+1}\right\}
$$

Because we know that $B_{l}$ and $B_{l+1}$ do not contain $Z$, we know (i.e. we can prove) that the result tree is a valid application of the invokevar rule. Illustration:

$$
\left(A_{l} \wedge B_{l}\right) \rightarrow\left(A_{l}^{\prime} \wedge B_{l}\right)[(\operatorname{shift}(w)) / Z]=\left(A_{l} \wedge B_{l}\right) \rightarrow A_{l}^{\prime}[(\operatorname{shift}(w)) / Z] \wedge B_{l}
$$

Example 9. Consider the following method fragment:

```
x = x^2;
```

and two proofs for it

$$
\begin{aligned}
& \{x \cdot x=y\} \\
& \mathbf{x}=\mathbf{x} * \mathbf{x} \\
& \{x=y\}
\end{aligned}
$$

together, applying the conjunct rule, they tell us that

$$
\begin{aligned}
& \{x \cdot x=y \wedge x \cdot x>0\} \\
& \mathbf{x}=\mathbf{x} * \mathbf{x} \\
& \{x=y \wedge x>0\}
\end{aligned}
$$

or representing the proof as a tree

$$
\frac{\overline{\{(\mathrm{x} \cdot \mathrm{x})=\mathrm{y}\}} \mathrm{x}=(\mathrm{x} \cdot \mathrm{x})\{\mathrm{x}=\mathrm{y}\}}{\{((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge((\mathrm{x} \cdot \mathrm{x})>0))\}} \quad \overline{\{(\mathrm{x} \cdot \mathrm{x})>0)\} x=(\mathrm{x} \cdot \mathrm{x})\{(\mathrm{x}>0)\}}
$$

Applying the $\nabla^{S}$ function on this tree gives us:

1. $\frac{(((x \cdot x)=y \wedge((x \cdot x)>0) \rightarrow(((x \cdot x)=y \wedge(x \cdot x)=(x \cdot x)) \wedge(((x \cdot x)>0) \wedge(x \cdot x)=(x \cdot x)))}{\{((x \cdot x)=y \wedge((x \cdot x)>0))\} 1: \operatorname{cop}((((x \cdot x)=y \wedge(x \cdot x)=(x \cdot x)) \wedge(((x \cdot x)>0) \wedge(x \cdot x)=(x \cdot x)))\}}$
2. $\frac{((((x \cdot x)=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x}))) \rightarrow(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x}))))}{\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})))\} 2: \operatorname{pushvar} \mathrm{x}\{((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})))\}}$


3. $\frac{((((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0))) \rightarrow(\mathrm{x}=\mathrm{y} \wedge(\mathrm{x}>0)))}{\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)))\} 5: \operatorname{pop} \mathrm{x}\{(\mathrm{x}=\mathrm{y} \wedge(\mathrm{x}>0))\}}$
or, in linear form
4. 

> nop
> $\{((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge((\mathrm{x} \cdot \mathrm{x})>0))\}$
> $1: \operatorname{nop}$
> $\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})))\}$
pushvar
$\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})))\}$
2 : pushvar x
$\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})))\}$

> pushvar
> $\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})))\}$
> $3:$ pushvar x
> $\{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))))\}$
4.

$$
\begin{aligned}
& \text { binop } \\
& \{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))))\} \\
& 4: \text { binop } \\
& \{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)))\}
\end{aligned}
$$

$$
\begin{aligned}
& \text { pop } \\
& \{(((\mathrm{x} \cdot \mathrm{x})=\mathrm{y} \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)))\} \\
& 5: \operatorname{pop} \mathrm{x} \\
& \{(\mathrm{x}=\mathrm{y} \wedge(\mathrm{x}>0))\}
\end{aligned}
$$

### 4.7.6 strength

$\nabla^{\mathrm{S}}\left(\operatorname{strength} \frac{P^{\prime} \rightarrow P \quad\{P\} S\{Q\}}{\left\{P^{\prime}\right\} S\{Q\}}\right)=$

$$
\begin{aligned}
P & :\left\{P^{\prime}\right\} \\
& \text { nop } \\
L & :\{P\} \\
& \nabla^{\mathrm{S}}(\{P\} S\{Q\}) \\
& \{Q\}
\end{aligned}
$$

## Reasons

$P$ : nop

$$
P^{\prime} \rightarrow P
$$

but this we know from the source proof. (see section 4 on page 22)

### 4.7.7 weak

$\nabla^{\mathrm{S}}\left(\right.$ weak $\left.\frac{Q \rightarrow Q^{\prime} \quad\{P\} S\{Q\}}{\{P\} S\left\{Q^{\prime}\right\}}\right)=$

$$
\begin{aligned}
& L:\{P\} \\
& \quad \nabla^{\mathrm{S}}(\{P\} S\{Q\}) \\
& P:\{Q\} \\
& \quad \operatorname{nop} \\
& \quad\left\{Q^{\prime}\right\}
\end{aligned}
$$

## Reasons

$P$ : nop We have to prove

$$
Q \rightarrow Q^{\prime}
$$

but this we know from the source proof. (see section 4 on page 22)

### 4.7.8 inv

$\nabla^{\mathrm{S}}\left(\operatorname{inv} \frac{\{P\} S\{Q\}}{\{P \wedge W\} S\{Q \wedge W\}}\right)=$
We just add a $\wedge W$ to every specification of the sequence produced by $\nabla^{\mathrm{S}}(\{P\} S\{Q\})$. We again exploit the special form of the rules.

$$
\frac{\frac{f(A) \rightarrow g(B)}{f(A) \wedge W \rightarrow g(B) \wedge W}}{\operatorname{distributive} \frac{\frac{\frac{\left(f^{-}(A) \wedge A_{f}\right) \wedge\left(W \wedge A_{f}\right) \rightarrow\left(g^{-}(B) \wedge A_{g}\right) \wedge\left(W \wedge A_{g}\right)}{}}{f(A) \wedge f(W) \rightarrow g(B) \wedge g(W)}}{\cdots f(A \wedge W) \rightarrow g(B \wedge W) \cdots}}
$$

invokevirtual is treated as in section 4.7 .5 on page 31 . We propagate the $\wedge W$ along the slim proof tree until we reach the method. We then apply the the original Java-K inv rule to the method.

Example 10. Consider this simple proof

$$
\begin{aligned}
& \{x \cdot x>0\} \\
& \mathbf{x}=\mathbf{x} * \mathbf{x} \\
& \{x>0\}
\end{aligned}
$$

again.
Applying the inv rule with $W \equiv(X=Y)$ yields

```
\(\Downarrow\{x \cdot x>0 \wedge X=Y\}\)
\(\{x \cdot x>0\}\)
\(\mathrm{x}=\mathrm{x} * \mathrm{x}\)
\(\{x>0\}\)
\(\Uparrow\{x>0 \wedge X=Y\}\)
```

and its proof tree

$$
\frac{\overline{\{((\mathrm{x} \cdot \mathrm{x})>0)\} x=(\mathrm{x} \cdot \mathrm{x})\{(\mathrm{x}>0)\}}}{\{(((\mathrm{x} \cdot \mathrm{x})>0) \wedge X=Y)\} x=(\mathrm{x} \cdot \mathrm{x})\{((\mathrm{x}>0) \wedge X=Y)\}}
$$

Applying the $\nabla^{S}$ function on this tree gives us what we expected:

1. $\frac{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge X=Y) \rightarrow((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge X=Y))}{\{(((\mathrm{x} \cdot \mathrm{x})>0) \wedge X=Y)\} 1: \operatorname{nop}\{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge X=Y)\}}$
2. $\frac{(((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge X=Y) \rightarrow((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge X=Y))}{\{(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(\mathrm{x} \cdot \mathrm{x})) \wedge X=Y)\} 2: \operatorname{pushvar} \mathrm{x}\{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})) \wedge X=Y)\}}$
3. $\frac{(((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot \mathrm{x})) \wedge X=Y) \rightarrow((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot \mathrm{x})) \wedge X=Y))}{\{(((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})) \wedge X=Y)\} 3: \operatorname{pushvar} \mathrm{x}\{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))) \wedge X=Y)\}}$
4. $\frac{(((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))) \wedge X=Y) \rightarrow((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))) \wedge X=Y))}{\{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=(s(1) \cdot s(0))) \wedge X=Y)\} 4: \text { binop } \cdot\{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge X=Y)\}}$
5. $\frac{(((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge X=Y) \rightarrow((\mathrm{x}>0) \wedge X=Y))}{\{((((\mathrm{x} \cdot \mathrm{x})>0) \wedge(\mathrm{x} \cdot \mathrm{x})=s(0)) \wedge X=Y)\} 5: \operatorname{pop} \mathrm{x}\{((\mathrm{x}>0) \wedge X=Y)\}}$

### 4.7.9 subst

$\nabla^{\mathrm{S}}\left(\right.$ subst $\left.\frac{\{P\} S\{Q\}}{\{P[t / Z]\} S\{Q[t / Z]\}}\right)=$ As before, first we generate $\nabla^{\mathrm{S}}(\{P\} S\{Q\})$ and then we replace $Z$ by $t$ in each specification and in all proofs for assertions (the antecedents normally). We again use the special form of our rules. We do not really need to do the replacement in all proof, we can actually replace the proofs compositionally:

$$
\frac{\frac{f(A) \rightarrow g(B)}{(f(A) \rightarrow g(B))[t / Z]}}{\frac{\cdots f(A[t / Z]) \rightarrow g(B[t / Z]) \cdots}{\left\{E^{(l)}[t / Z]\right\} l: \operatorname{instr}\left\{E^{(l+1)}[t / Z]\right\}}}
$$

invokevirtual is treated as in section 4.7 .8 on the preceding page.

### 4.8 Rules for References, Objects and Dynamic Dispatch

### 4.8.1 invocation

$$
\begin{gathered}
\nabla^{\mathrm{S}}\left(\text { invocation } \frac{\{P\} T: m\{Q\}}{\{y \neq \operatorname{null} \wedge P[y / \text { this }, e / \mathrm{p}]\} x=y \cdot T: m(e)\{Q[x / \text { result }]\}}\right)= \\
S:\left\{Q_{s} \equiv y \neq \text { null } \wedge P[y / \text { this, } e / \mathrm{p}]\right\} \\
\text { pushvar } y \\
V:\left\{\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right\} \\
\nabla^{\mathrm{E}}\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y, e, Q_{i} \equiv \operatorname{shift}\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right) \wedge s(0)=e\right) \\
I:\left\{Q_{i}\right\} \\
\text { invokevirtual } T: m \\
A:\{Q[s(0) / \text { result }]\} \\
\operatorname{pop} x \\
\{Q[x / \text { result }]\}
\end{gathered}
$$

## Reasons

$A$ : pop $x \quad Q$ does not contain program variables or stack references

$$
\begin{aligned}
Q[s(0) / \text { result }] & \rightarrow(\operatorname{shift}(Q[x / \text { result }]))[s(0) / x] \\
=Q[s(0) / \text { result }] & \rightarrow Q[x / \text { result }][s(0) / x] \\
=Q[s(0) / \text { result }] & \rightarrow Q[s(0) / \text { result, } s(0) / x] \\
=Q[s(0) / \text { result }] & \rightarrow Q[s(0) / \text { result }]
\end{aligned}
$$

$S$ : pushvar $y$

$$
\operatorname{shift} Q_{s} \rightarrow\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right)[x / s(0)]
$$

$I$ : invokevirtual $T: m$

$$
\begin{aligned}
& Q_{i} \\
& \rightarrow s(1) \neq \operatorname{null} \wedge P[s(1) / \text { this, } s(0) / \mathrm{p}] \\
= & \operatorname{shift}\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right) \wedge s(0)=e \\
& \rightarrow s(1) \neq \text { null } \wedge P[s(1) / \text { this, } s(0) / \mathrm{p}] \\
= & Q_{s} \wedge s(1)=y \wedge s(0)=e \\
& \rightarrow s(1) \neq \text { null } \wedge P[s(1) / \text { this }, s(0) / \mathrm{p}] \\
= & y \neq \text { null } \wedge P[y / \text { this }, e / \mathrm{p}] \wedge s(1)=y \wedge s(0)=e \\
& \rightarrow s(1) \neq \text { null } \wedge P[s(1) / \text { this, } s(0) / \mathrm{p}]
\end{aligned}
$$

### 4.8.2 invocation-var

$\nabla^{\mathrm{S}}\left(\right.$ invocation-var $\left.\frac{\{P\} x=y \cdot T: m(e)\{Q\}}{\{P[w / Z]\} x=y \cdot T: m(e)\{Q[w / Z]\}}\right)=$
generate the proof $\nabla^{\mathrm{E}}(P, x=y \cdot T: m(e), Q)$ and replace all $Z$ by $w$ in all instruction proofs. The proofs are still going to work, because the instructions pushvar, pushc and binop ${ }_{o p}$ generated by $\nabla^{\mathrm{E}}$ and $\nabla^{\mathrm{X}}$ (for the actual parameter $e$ ) do not modify local variables ( $w$ in our case) The proof-checker does not need to know that, it just sees that the deductions are still correct, which is what we want to achieve.

In fact, we need not treat invocation-var any different than the subst-rule. The only difference is that we replace a local variable instead of a logical variable. $f, g$ are not commutative with respect to replacement of logical variables with local variables in general. But as explained in the previous paragraph, we also know a lot more (see also section 4.6 on page 27 and section 4.8 .1 on the preceding page): we know that for the evaluation of expressions, only rules with $f, g \in\{\operatorname{shift},().[\cdots / s(0)],(\operatorname{shift}()).[(s(1)$ op $s(0)) / s(1)]\}$ are generated, the variable $w$ we may introduce is not used in the assignment. Taking these facts into account, we can safely treat invocation-var for all rules exactly as the subst-rule.

$$
\frac{\frac{\cdots}{f(A) \rightarrow g(B)}}{\frac{\cdots f(A[w / Z]) \rightarrow g(B[w / Z]) \cdots}{(f(A) \rightarrow g(B))[w / Z]}} \frac{\left\{E^{(l)}[w / Z]\right\} l: \operatorname{instr}\left\{E^{(l+1)}[w / Z]\right\}}{}
$$

invokevirtual is treated similarly as in section 4.7 .5 on page 31 by insertion of the specialized invokevar rule.

Note that we can restrict our attention to local variables and we do not need to include stack elements in our consideration although that would also be possible with our invokevar rule (section 3.4.3 on page 18).

Example 11. Consider the virtual method invocation:

$$
\begin{gathered}
\text { invoke } \frac{\left\{\mathrm{p}=p_{0}\right\} \operatorname{Add} 2\left\{\text { result }=\left(p_{0}+2\right)\right\}}{\left\{\mathrm{p}=p_{0}[\mathrm{x} / \mathrm{p}][\mathrm{o} / \text { this }]\right\} x=o . A d d 2(\mathrm{x})\left\{\text { result }=\left(p_{0}+2\right)[\mathrm{x} / \text { result }]\right\}} \\
\operatorname{inv} \frac{\left.\sin \left(\mathrm{p}=p_{0}[\mathrm{x} / \mathrm{p}][\mathrm{o} / \text { this }] \wedge Z=5\right)\right\} x=o . A d d 2(\mathrm{x})\left\{\left(\text { result }=\left(p_{0}+2\right)[\mathrm{x} / \text { resul } \mathrm{t}] \wedge Z=5\right)\right\}}{\left\{\left(\mathrm{p}=p_{0}[\mathrm{x} / \mathrm{p}][\mathrm{o} / \text { this }] \wedge Z=5\right)[\mathrm{t} / Z]\right\} \quad x=o . A d d 2(\mathrm{x})\left\{\left(\text { result }=\left(p_{0}+2\right)[\mathrm{x} / \text { resul }] \wedge Z=5\right)[\mathrm{t} / Z]\right\}}
\end{gathered}
$$

simplified:

$$
\begin{gathered}
\text { invoke } \frac{\left.\left\{\mathrm{p}=p_{0}\right\} \text { Add } 2 \text { result }=\left(p_{0}+2\right)\right\}}{\left\{\mathrm{x}=p_{0}\right\} x=o . A d d 2(\mathrm{x})\left\{\mathrm{x}=\left(p_{0}+2\right)\right\}} \\
\operatorname{inv} \frac{\sin }{\left\{\left(\mathrm{x}=p_{0} \wedge Z=5\right)\right\} x=o . A d d 2(\mathrm{x})\left\{\left(\mathrm{x}=\left(p_{0}+2\right) \wedge Z=5\right)\right\}} \\
\left\{\left(\mathrm{x}=p_{0} \wedge \mathrm{t}=5\right)\right\} x=o . \operatorname{Add2(\mathrm {x})\{ (\mathrm {x}=(p_{0}+2)\wedge \mathrm {t}=5)\} }
\end{gathered}
$$

Applying $\nabla^{\mathrm{S}}$ yields:

1. $\overline{\left\{\left(\mathrm{x}=p_{0} \wedge \mathrm{t}=5\right)\right\} 1: \text { nop }\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=\mathrm{o}\right) \wedge \mathrm{t}=5\right)\right\}}$
2. $\overline{\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=\mathrm{o}\right) \wedge \mathrm{t}=5\right)\right\} 2: \text { pushvar } \circ\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{t}=5\right)\right\}}$
3. $\overline{\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{t}=5\right)\right\} 3: \operatorname{nop}\left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{x}=\mathrm{x}\right) \wedge \mathrm{t}=5\right)\right\}}$
4. $\overline{\left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{x}=\mathrm{x}\right) \wedge \mathrm{t}=5\right)\right\} 4: \text { pushvar } \mathrm{x}\left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(1)\right) \wedge \mathrm{x}=s(0)\right) \wedge \mathrm{t}=5\right)\right\}}$

$$
\frac{\left.\left\{\mathrm{p}=p_{0}\right\} \text { Add } 2 \text { \{result }=\left(p_{0}+2\right)\right\}}{\left\{\left(\mathrm{p}=p_{0} \wedge Z=5\right)\right\} \text { Add } 2\left\{\left(\text { result }=\left(p_{0}+2\right) \wedge Z=5\right)\right\}}
$$

5. $\frac{\left.\left.\left.\overline{\left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \circ=s(1)\right) \wedge \mathrm{x}=s(0)\right) \wedge Z=5\right)\right\}} \frac{5: \text { invokevirtual } A d d 2\left\{\left(s(0)=\left(p_{0}+2\right) \wedge Z=5\right)\right\}}{\left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \circ=s(1)\right) \wedge \mathrm{x}=s(0)\right) \wedge \mathrm{t}=5\right)\right\} 5} \text { : invokevirtual Add2\{(s(0)=(} p_{0}+2\right) \wedge \mathrm{t}=5\right)\right\}}{\text { A }}$
6. $\overline{\left\{\left(s(0)=\left(p_{0}+2\right) \wedge t=5\right)\right\} 6: \operatorname{pop} \mathrm{x}\left\{\left(\mathrm{x}=\left(p_{0}+2\right) \wedge \mathrm{t}=5\right)\right\}}$
or in linear form:
7. 

nop
$\left\{\left(\mathrm{x}=p_{0} \wedge \mathrm{t}=5\right)\right\}$
$1: \operatorname{nop}$
$\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=\mathrm{o}\right) \wedge \mathrm{t}=5\right)\right\}$
2.

> pushvar
> $\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=\mathrm{o}\right) \wedge \mathrm{t}=5\right)\right\}$
> $2:$ pushvar $\circ$
> $\left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{t}=5\right)\right\}$
3.

$$
\begin{aligned}
& \text { nop } \\
& \left\{\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{t}=5\right)\right\} \\
& 3: \text { nop } \\
& \left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{x}=\mathrm{x}\right) \wedge \mathrm{t}=5\right)\right\}
\end{aligned}
$$

4. 

$$
\begin{aligned}
& \text { pushvar } \\
& \left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(0)\right) \wedge \mathrm{x}=\mathrm{x}\right) \wedge \mathrm{t}=5\right)\right\} \\
& 4: \text { pushvar } \mathrm{x} \\
& \left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(1)\right) \wedge \mathrm{x}=s(0)\right) \wedge \mathrm{t}=5\right)\right\}
\end{aligned}
$$

> invokevar
> $\left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(1)\right) \wedge \mathrm{x}=s(0)\right) \wedge \mathrm{t}=5\right)\right\}$
5.
$5: \begin{aligned} & \text { invokevirt } \\ & \left\{\left(\left(\left(\mathrm{x}=p_{0} \wedge \mathrm{o}=s(1)\right) \wedge \mathrm{x}=\right.\right.\right. \\ & 5: \text { invokevirtual } A d d 2 \\ & \left\{\left(s(0)=\left(p_{0}+2\right) \wedge Z=5\right)\right\}\end{aligned}$
$\left\{\left(s(0)=\left(p_{0}+2\right) \wedge \mathrm{t}=5\right)\right\}$
6.

$$
\begin{aligned}
& \text { pop } \\
& \left\{\left(s(0)=\left(p_{0}+2\right) \wedge \mathrm{t}=5\right)\right\} \\
& 6: \operatorname{pop} \mathrm{x} \\
& \left\{\left(\mathrm{x}=\left(p_{0}+2\right) \wedge \mathrm{t}=5\right)\right\}
\end{aligned}
$$

4.8.3 cast
$\nabla^{\mathrm{S}}(\operatorname{cast} \overline{\{\tau(e) \preceq T \wedge P[e / x]\} \quad x=(T) e\{P\}})=$

$$
\begin{aligned}
V: & \left\{Q_{s} \equiv \tau(e) \preceq T \wedge P[e / x]\right\} \\
& \nabla^{\mathrm{E}}\left(Q_{s}, e, \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=e\right) \\
C: & \left\{\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=e\right\} \\
& \operatorname{checkcast} \\
A: & \left\{\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=e\right\} \\
& \operatorname{pop} x \\
& \{P\}
\end{aligned}
$$

## Reasons

$C$ : checkcast

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=e \rightarrow \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=e \wedge \tau(s(0)) \preceq T \\
= & \tau(e) \preceq T \wedge P[e / x] \wedge s(0)=e \rightarrow \tau(e) \preceq T \wedge P[e / x] \wedge s(0)=e \wedge \tau(s(0)) \preceq T
\end{aligned}
$$

$A$ : pop $x$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \\
& \wedge s(0)=e \rightarrow(\operatorname{shift}(P))[s(0) / x] \\
= & \tau(e) \preceq T \wedge P[e / x] \wedge s(0)=e \rightarrow P[s(0) / x]
\end{aligned}
$$

### 4.8.4 construct

$$
\left.\begin{array}{c}
\nabla^{\mathrm{S}}\left(\text { construct } \frac{}{\{P[\operatorname{new}(\$, T) / x, \$\langle T\rangle / \$]\}} x=\operatorname{new} T()\{P\}\right.
\end{array}\right)=
$$

Reasons
$V$ : newobj $T$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \\
& \rightarrow((\operatorname{shift}(P))[s(0) / x])[\operatorname{new}(\$, T) / s(0), \$\langle T\rangle / \$] \\
= & Q_{s} \\
& \rightarrow P[s(0) / x][\operatorname{new}(\$, T) / s(0), \$\langle T\rangle / \$] \\
= & Q_{s} \\
& \rightarrow P[\operatorname{new}(\$, T) / x, \operatorname{new}(\$, T) / s(0), \$\langle T\rangle / \$] \\
= & Q_{s} \\
& \rightarrow \underbrace{P[\operatorname{new}(\$, T) / x, \$\langle T\rangle / \$]}_{=Q_{s}}
\end{aligned}
$$

$A$ : pop $x$

$$
\begin{aligned}
& (\operatorname{shift}(P))[s(0) / x] \\
& \quad \rightarrow(\operatorname{shift}(P))[s(0) / x]
\end{aligned}
$$

### 4.8.5 read

$$
\begin{gathered}
\nabla^{\mathrm{S}}\left(\operatorname{read} \frac{}{\{y \neq \operatorname{null} \wedge P[\$(\operatorname{iv}(y, S @ a)) / x]\} x=y \cdot S @ a\{P\}}\right)= \\
S:\left\{Q_{s} \equiv y \neq \operatorname{null} \wedge P[\$(\operatorname{iv}(y, S @ a)) / x]\right\} \\
\operatorname{pushvar} y \\
V:\left\{\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right\} \\
\operatorname{getfield} S @ a \\
A:\left\{\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=\$(\operatorname{iv}(y, S @ a))\right\} \\
\operatorname{pop} x \\
\{P\}
\end{gathered}
$$

## Reasons

$S$ : pushvar $y$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \\
= & \left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right)[y / s(0)] \\
\operatorname{shift}\left(Q_{s}\right) & \rightarrow \operatorname{shift}\left(Q_{s}\right) \wedge y=y
\end{aligned}
$$

$V:$ getfield $S @ a$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y \\
& \rightarrow\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=\$(\operatorname{iv}(y, S @ a))\right)[\$(\operatorname{iv}(s(0), S @ a)) / s(0)] \wedge s(0) \neq \text { null } \\
= & \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y \\
& \rightarrow\left(\operatorname{shift}\left(Q_{s}\right) \wedge \$(\operatorname{iv}(s(0), S @ a))=\$(\operatorname{iv}(y, S @ a))\right)[\$(\operatorname{iv}(s(0), S @ a)) / s(0)] \wedge s(0) \neq \text { null } \\
= & \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y \\
& \rightarrow\left(\operatorname{shift}\left(Q_{s}\right)\right)[\$(\operatorname{iv}(s(0), S @ a)) / s(0)] \wedge s(0) \neq \operatorname{null} \\
= & Q_{s} \wedge s(0)=y \\
& \rightarrow Q_{s} \wedge s(0) \neq \operatorname{null} \\
= & \underbrace{\left(Q_{s} \wedge y \neq \operatorname{null}\right)}_{Q_{s}} \wedge s(0)=y \\
& \rightarrow Q_{s} \wedge s(0) \neq \operatorname{null}
\end{aligned}
$$

## $A$ : pop $x$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \wedge s(0)=\$(\operatorname{iv}(y, S @ a)) \rightarrow(\operatorname{shift}(P))[s(0) / x] \\
= & Q_{s} \wedge s(0)=\$(\operatorname{iv}(y, S @ a)) \\
& \rightarrow P[s(0) / x] \\
= & y \neq \operatorname{null} \wedge P[\$(\operatorname{iv}(y, S @ a)) / x] \wedge s(0)=\$(\operatorname{iv}(y, S @ a)) \\
& \rightarrow P[s(0) / x]
\end{aligned}
$$

### 4.8.6 write

$$
\begin{gathered}
\nabla^{\mathrm{S}}\left(\text { write } \frac{}{\{y \neq \operatorname{null} \wedge P[\$\langle\operatorname{iv}(y, S @ a):=e\rangle / \$]\} y \cdot S @ a=e\{P\}}\right)= \\
S:\left\{Q_{s} \equiv y \neq \operatorname{null} \wedge P[\$\langle\operatorname{iv}(y, S @ a):=e\rangle / \$]\right\} \\
\text { pushvar } y \\
V:\left\{\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right\} \\
\nabla^{\mathrm{E}}\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y, e, Q_{x} \equiv \operatorname{shift}\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right) \wedge s(0)=e\right) \\
A:\left\{Q_{x}\right\} \\
\\
\text { putfield } S @ a \\
\{P\}
\end{gathered}
$$

## Reasons

$S$ : pushvar $y$

$$
\begin{aligned}
& \operatorname{shift}\left(Q_{s}\right) \\
= & \left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right)[y / s(0)] \\
=\operatorname{shift}\left(Q_{s}\right) & \rightarrow \operatorname{shift}\left(Q_{s}\right) \wedge y=y
\end{aligned}
$$

$$
\begin{aligned}
& Q_{x} \rightarrow \underbrace{\left(\operatorname{shift}^{2}(P)\right)[\$\langle\operatorname{iv}(s(1), S @ a):=s(0)\rangle / \$] \wedge s(1) \neq \operatorname{null}}_{G} \\
= & \operatorname{shift}\left(\operatorname{shift}\left(Q_{s}\right) \wedge s(0)=y\right) \wedge s(0)=e \rightarrow G \\
= & \operatorname{shift}^{2}\left(Q_{s}\right) \wedge s(1)=y \wedge s(0)=e \rightarrow G \\
= & y \neq \operatorname{null} \wedge \underbrace{\operatorname{shift}^{2}(P[\$\langle\operatorname{iv}(y, S @ a):=e\rangle / \$])}_{\text {does not contain references to the stack }} \wedge s(1)=y \wedge s(0)=e \rightarrow G \\
= & y \neq \operatorname{null} \wedge P[\$\langle\operatorname{iv}(y, S @ a):=e\rangle / \$] \wedge s(1)=y \wedge s(0)=e \rightarrow G \\
= & P[\$\langle\operatorname{iv}(y, S @ a):=e\rangle / \$] \wedge y \neq \text { null } \wedge s(1)=y \wedge s(0)=e \\
& \rightarrow P[\$\langle\operatorname{iv}(s(1), S @ a):=s(0)\rangle / \$] \wedge s(1) \neq \text { null }
\end{aligned}
$$

### 4.9 Methods

$\nabla^{\mathrm{S}}\left(\frac{\{P\} \operatorname{body}(T @ m)\{Q\}}{\{P\} T @ m\{Q\}}\right)=$

$$
\mathfrak{p}=\left[\begin{array}{l}
S:\left\{P^{\prime}=P \wedge \text { this } \neq \text { null }\right\} \\
\nabla^{\mathrm{S}}\left(\left\{P^{\prime}\right\} \text { body }(T @ m)\{Q\}\right) \\
T:\{Q\} \\
\text { end_method } \\
\{Q\}
\end{array}\right] \begin{array}{r}
I_{|\mathfrak{p}|}=\text { end_method } \\
\forall i<|\mathfrak{p}|: \mathfrak{p}(i) \neq \text { end_method } \\
\text { precondition }_{\mathfrak{p}}(1)=P \wedge \text { this } \neq \text { null } \\
\text { postcondition }_{\mathfrak{p}}(|\mathfrak{p}|)=Q
\end{array}
$$

## 5 Application

In this section, we present a Java-K proof and its automatic translation in order to give a feeling together with implementation in section A on page 49 - how the translation rules are used. The example is necessarily trivial. That's why it does not use some of the more complex instruction rules. It calculates the factorial of a number.

We first give a linear form of the proof, then the actual proof tree and its translation into a $\mathrm{VM}_{\mathrm{K}}$ proof.

### 5.1 Java-K, linear

```
public class Trivial {
    public int f(int x) {
        {x=\mp@subsup{x}{0}{}}
        result = 1;
        {x=\mp@subsup{x}{0}{}\wedge result =1}
        \Longrightarrow
        {result }\cdotx!=\mp@subsup{x}{0}{\prime!}
        while(x != 0){
            {x\not=0\wedge result }\cdotx!=\mp@subsup{x}{0}{}!
            \Longrightarrow
```

$$
\begin{aligned}
& \left\{\text { result } \cdot x \cdot(x-1)!=x_{0}!\right\} \\
& \text { result = result*x; } \\
& \text { \{result } \left.\cdot(x-1)!=x_{0}!\right\} \\
& \mathrm{x}=\mathrm{x}-1 \text {; } \\
& \left\{\text { result } \cdot x!=x_{0}!\right\} \\
& \text { \} } \\
& \left\{\text { result } \cdot x!=x_{0}!\wedge x=0\right\} \\
& \Longrightarrow \\
& \left\{\text { result }=x_{0}!\right\} \\
& \text { \} }
\end{aligned}
$$

\}

### 5.2 Java-K, Tree

$$
\begin{aligned}
& T_{0}=\frac{\mathrm{id} \frac{T_{1}}{\{P\} \text { bod } y_{\mathrm{VM}_{\mathrm{K}}}(\text { Trivial@f) }\{Q\}}}{\{P\} \text { Trivial@f }\{Q\}} \\
& \cdots \text { see [PHM99] } \\
& \overline{\left\{P \equiv x=x_{0}\right\} \text { Trivial : } \mathrm{f}\left\{Q \equiv \text { result }=x_{0}!\right\}} \\
& T_{1}=\operatorname{weak} \frac{\operatorname{seq} \frac{T_{2}}{\left\{x=x_{0}\right\} \text { result }=1 ; \text { while }(\ldots) \ldots\left\{\text { result } \cdot x!=x_{0}!\wedge x=0\right\}}}{\left\{x=x_{0}\right\} \text { result }=1 ; \text { while }(\ldots) \ldots\left\{\text { result }=x_{0}!\right\}} \\
& T_{2}=\text { weak } \frac{\text { assign } \overline{\left\{x=x_{0}\right\} \text { result }=1\left\{x=x_{0} \wedge \text { result }=1\right\}}}{\left\{x=x_{0}\right\} \text { result }=1\left\{\text { result } \cdot x!=x_{0}!\right\}} \\
& T_{3}=\operatorname{while} \frac{\operatorname{seq} \frac{T_{4} \quad T_{5}}{\left\{x \neq 0 \wedge \text { result } \cdot x!=x_{0}!\right\} \ldots ; \ldots\left\{\text { result } \cdot x!=x_{0}!\right\}}}{\left\{\text { result } \cdot x!=x_{0}!\right\} \text { while }(\ldots) \ldots\left\{\text { result } \cdot x!=x_{0}!\wedge x=0\right\}} \\
& T_{4}=\operatorname{strength} \frac{\operatorname{assign} \overline{\left\{\text { result } \cdot x \cdot(x-1)!=x_{0}!\right\} \text { result }=\text { result } \cdot \mathrm{x} ;\left\{\text { result } \cdot(x-1)!=x_{0}!\right\}}}{\left\{x \neq 0 \wedge \text { result } \cdot x!=x_{0}!\right\} \text { result }=\text { result } \cdot x ;\left\{\text { result } \cdot(x-1)!=x_{0}!\right\}} \\
& T_{5}=\operatorname{assign} \overline{\left\{\operatorname{result} \cdot(x-1)!=x_{0}!\right\} \mathrm{x}=\mathrm{x}-1 ;\left\{\operatorname{result} \cdot x!=x_{0}!\right\}}
\end{aligned}
$$

### 5.3 The Translation

The translation has been automatically generated from the source proof. result is abbreviated by r (to save space). "Reasons" for the antecedents are not given but they are trivial to check. The assumption this $\neq$ null is left implicit in our assertions.

$$
\begin{aligned}
& {\left[\begin{array}{l}
\overline{\left\{\mathrm{x}=x_{0}\right\} 1: \operatorname{nop}\left\{\left(\mathrm{x}=x_{0} \wedge 1=1\right)\right\}} \\
\overline{\left\{\left(\mathrm{x}=x_{0} \wedge 1=1\right)\right\} 2: \operatorname{pushc} 1\left\{\left(\mathrm{x}=x_{0} \wedge 1=s(0)\right)\right\}} \\
\overline{\left\{\left(\mathrm{x}=x_{0} \wedge 1=s(0)\right)\right\} 3: \operatorname{pop} \mathrm{r}\left\{\left(\mathrm{x}=x_{0} \wedge \mathrm{r}=1\right)\right\}} \\
\left\{\left(\mathrm{x}=x_{0} \wedge \mathrm{r}=1\right)\right\} 4: \text { nop }\left\{(\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\right\} \\
\overline{\left\{(\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\right\} 5: \text { goto } 17\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge \mathrm{x} \neq 0\right)\right\}} \\
\overline{\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge \mathrm{x} \neq 0\right)\right\} 6: \operatorname{nop}\left\{((\mathrm{r} \cdot \mathrm{x}) \cdot((\mathrm{x}-1)!))=x_{0}!\right\}} \\
\overline{\left\{((\mathrm{r} \cdot \mathrm{x}) \cdot((\mathrm{x}-1)!))=x_{0}!\right\} 7: \text { nop }\left\{\left(((\mathrm{r} \cdot \mathrm{x}) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{r} \cdot \mathrm{x})=(\mathrm{r} \cdot \mathrm{x})\right)\right\}}
\end{array}\right.} \\
& \overline{\left\{\left(((r \cdot x) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{r} \cdot \mathrm{x})=(\mathrm{r} \cdot \mathrm{x})\right)\right\} 8: \operatorname{pushvar} \mathrm{r}\left\{\left(((\mathrm{r} \cdot \mathrm{x}) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{r} \cdot \mathrm{x})=(s(0) \cdot \mathrm{x})\right)\right\}} \\
& \overline{\left\{\left(((r \cdot x) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathbf{r} \cdot \mathbf{x})=(s(0) \cdot \mathbf{x})\right)\right\} 9: \text { pushvar } \mathrm{x}\left\{\left(((\mathrm{r} \cdot \mathbf{x}) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{r} \cdot \mathbf{x})=(s(1) \cdot s(0))\right)\right\}} \\
& \overline{\left\{\left(((\mathbf{r} \cdot \mathbf{x}) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathbf{r} \cdot \mathbf{x})=(s(1) \cdot s(0))\right)\right\} 10: \text { binop } \cdot\left\{\left(((\mathbf{r} \cdot \mathbf{x}) \cdot((\mathbf{x}-1)!))=x_{0}!\wedge(\mathbf{r} \cdot \mathbf{x})=s(0)\right)\right\}} \\
& \overline{\left\{\left(((r \cdot x) \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{r} \cdot \mathbf{x})=s(0)\right)\right\} 11: \operatorname{pop} \mathrm{r}\left\{(\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\right\}} \\
& \overline{\left\{(\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\right\} 12: \operatorname{nop}\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=(\mathrm{x}-1)\right)\right\}} \\
& \overline{\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=(\mathrm{x}-1)\right)\right\} 13: \operatorname{push} \operatorname{car} \mathrm{x}\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=(s(0)-1)\right)\right\}} \\
& \overline{\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=(s(0)-1)\right)\right\} 14: \text { pushc } 1\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=(s(1)-s(0))\right)\right\}} \\
& \overline{\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=(s(1)-s(0))\right)\right\} 15: \operatorname{binop}-\left\{\left((\mathrm{r} \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=s(0)\right)\right\}} \\
& \overline{\left\{\left((r \cdot((\mathrm{x}-1)!))=x_{0}!\wedge(\mathrm{x}-1)=s(0)\right)\right\} 16: \operatorname{pop} \mathrm{x}\left\{(\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\right\}} \\
& \overline{\left\{(r \cdot(\mathrm{x}!))=x_{0}!\right\} 17: \operatorname{nop}\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=(\mathrm{x} \neq 0)\right)\right\}} \\
& \overline{\left\{\left((r \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=(\mathrm{x} \neq 0)\right)\right\} 18: \text { pushvar } \mathrm{x}\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=(s(0) \neq 0)\right)\right\}} \\
& \overline{\left\{\left((r \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=(s(0) \neq 0)\right)\right\} 19: \text { pushc } 0\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=(s(1) \neq s(0))\right)\right\}} \\
& \overline{\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=(s(1) \neq s(0))\right)\right\} 20: \text { binop } \neq \quad\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=s(0)\right)\right\}} \\
& \overline{\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge(\mathrm{x} \neq 0)=s(0)\right)\right\} 21: \text { brtrue } 6\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge \mathrm{x}=0\right)\right\}} \\
& \overline{\left\{\left((\mathrm{r} \cdot(\mathrm{x}!))=x_{0}!\wedge \mathrm{x}=0\right)\right\} 22: \operatorname{nop}\left\{\mathrm{r}=x_{0}!\right\}} \\
& \overline{\left\{\mathrm{r}=x_{0}!\right\} 23: \text { end_method }\left\{\mathrm{r}=x_{0}!\right\}} \\
& \left\{\mathbf{x}=x_{0}\right\} \text { Trivial } @ f\left\{\mathbf{r}=x_{0}!\right\}
\end{aligned}
$$

## 6 Related Work

A huge amount of work deals with the formalization of aspects of the JVM. [HM01] contains an overview. [SBS01] gives an almost comprehensive ASM specification of the JVM. Operational semantics for the JVM are given in many different publications. Most of them want to be faithful to the "real" JVM but fail to model non-trivial aspects like dynamic class loading and garbage collection. Examples include [Qia99], [SH01] and [Ber97]. Operational semantics have been used to proof the soundness of type systems for bytecode (e.g., [SNF03]), but they are not very suitable for program verification. [Qui03] describes a formalism that tries to rediscover structures in the bytecode for program verification, precluding the
verification of arbitrary programs. There are fewer papers about the CLR, but the results are the same. The formalism of our logic is based on [Ben04].

## 7 Conclusion

We have presented the operational semantics and a programming logic for the bytecode of $\mathrm{VM}_{\mathrm{K}}$, a virtual machine similar to the JVM or the CLR. Possible uses of this approach include language interoperability of specifications and trusted components that are not available in source form.

To show complex properties of a program, we usually have to produce a proof by hand. This is done on the source level and not on the bytecode level. In order to make applications of a bytecode logic possible, we therefore need an automatic translation procedure from source proofs. We have presented such a translation procedure together with a simple prototype implementation.

The bytecode logic is a simple Hoare style logic with labeled assertions. Labeled assertions allow us to express non-local requirements on instructions. They are conceptually simpler than other methods to handle unstructured control flows. Checking a proof is purely local: we can check one instruction at a time.

The simplicity of our proof translation is fundamentally tied to the special shape ${ }^{13}$ of the deductive rules we introduced. It allowed us to omit instruction independent rules like the weak, inv, subst rules from our bytecode logic. Unfortunately, we were not able to translate the ex- and all-rules to our logic.

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## A Implementation

This appendix contains an implementation of the translation in SML. It is meant to demonstrate that the translation is easy.

The "reasons" sections did not find their way into this translation because we did not want to restrict ourselves to specific underlying logic and a deductive system for that logic. We do not include the trivial invariant this $\neq$ null in our proofs, either.

The actual compilation does not begin before line 217 with the function CompileX. Three functions are defined that correspond to the three translation functions defined in section 4 on page 22:

| SML function name | Symbol |
| :--- | :--- |
| CompileX | $\nabla^{\mathrm{X}}$ |
| CompileE | $\nabla^{\mathrm{E}}$ |
| CompileS | $\nabla^{\mathrm{S}}$ |

The translation from the definitions in the text to the runnable SML program is quite direct. Still, the definition of the more difficult translations (line 243) may be interesting when an existing verified instruction sequence is modified (for the substitution rule for instance).

The translation function CompileImpl (line 396) is just a distinct name for $\nabla^{S}$ when method implementations should be translated, which is our ultimate goal.

The generation of labels is quite mundane: the genuniqueid (line 115) is a helper function to produce fresh variables and labels. It works by updating a counter. In order to get the numbers right, we take care to do the recursive invocation of the translation functions in the order in which their results will appear in the final instruction sequence.

The functions at line 154 are used for the simplification of expressions. They are not strictly necessary, but help when producing printable output.

Because there is only one rule for most instructions, we declare (line 199) the function that does the mapping between instructions and rules.

The rest of the code is straight-forward and should not need any additional explanation.

- The datatype Expr (line 3) is used to represent all assertions/predicates as well as expressions in the program. Operations that are strictly required to handle the proofs are included (e.g. conjunction,
disjunction) plus a few others. prg, lvr, stk represent program, logical and stack variables. There is one operation for the replacement of each kind of variables.
- We treat the object-store $\$$ (line 28 ) just like an ordinary program variable.
- Stmt (line 30) represents arbitrary computations both for Java-K and $\mathrm{VM}_{\mathrm{K}}$.
- Rule (line 60) is an enumeration of all possible rules. Again, there is no separation between JavaK and $\mathrm{VM}_{\mathrm{K}}$. The antecedents are not stored with the individual rules in order to avoid lengthy repetitions of similar declarations.
- Proof (line 102) stores an actual proof. There are two kinds of proofs: those constructed using DT, representing proofs for Hoare-triples and those that prove assertions (constructed with DE). As before, it may be safer to treat Hoare-triple proofs and assertion proofs as different datatypes, but the unification chosen here aims at making the program smaller.
- Starting at line 104, some accessors for the parts of the proofs are declared, we use them because the DT constructor is quite complicated.

```
    (* Transformation of SJ-Proofs to SVM-Proofs *)
    (* Predicates/Assertions for SJ/SVM *)
    datatype Expr = falseP | trueP
            | implies of Expr * Expr
            | conj of Expr * Expr
            | disj of Expr * Expr
                (* for lur's *)
            | allP of string * Expr
            | exP of string * Expr
            | shiftP of Expr
            | unshiftP of Expr
            (* relational operators *)
            | eqP of Expr * Expr
            | neP of Expr * Expr
            | binop of string * Expr * Expr
            | unop of string * Expr
            | prg of string
            | lvr of string
            | stk of int
            | genop of string * Expr list
            | replacelvr of string * Expr * Expr
            | replaceprg of string * Expr * Expr
            | replacestk of int * Expr * Expr
            | literal of string
                                    (* OS *)(*obj*)(*field*)
            | instvar of Expr * Expr * string
                            | instvarupdate of Expr * Expr * string * Expr
    val OS = prg "$"
    (* SJ Stmts and Methods *)
datatype Stmt = cast of string * string * Expr
            | ctor of string * string
            | assign of string * Expr
            | readf of string * string * string
            | writef of string * string * Expr
            | invoke of string * string * string * Expr
            | swhile of Expr * Stmt
            | sif of Expr * Stmt * Stmt
            | seq of Stmt * Stmt
            | skipstmt
            | kmeth of string (* T@m *)
            | vmeth of string
            | pushc of string
            | pushvar of string
            | pop of string
            | iunop of string
            | ibinop of string
            | goto of string (* labels are strings *)
            | brtrue of string
            | checkcast of string
            | newobj of string
            | invokevirtual of string
            | getfield of string
            | putfield of string
            | methodend
            | nop
(*
            at the moment, no reaons are given, trees are
            trees are just for hoare-triples
```

*)
o datatype Rule = assumed
| omitted
l rpushc
| rpushvar
| rpop
and
(* label, instr, precond, post, rule, antecedents *)
Proof $=$ DT of string * Stmt * Expr * Expr * Rule * Proof list
| DE of Expr (* result *)
(**** PROOF TRANSFORMATION BEGIN ***********)
105 fun antecedents(DT(label, instr, pre, post, rule, antes)) = antes
fun precondition(DT(label, instr, pre, post, rule, antes)) = pre
fun postcondition(DT(label, instr, pre, post, rule, antes)) = post
fun instruction(DT(label, instr, pre, post, rule, antes)) = instr
fun label(DT(label, instr, pre, post, rule, antes)) = label
110 fun first $\mathrm{L}=\mathrm{hd} \mathrm{L}$
fun last L = List.last L
fun strictzip (a::x) (b::z) = (a,b)::(strictzip x z)
| strictzip [] [] = []
(* generating fresh variables *)

```
1 1 5 ~ f u n ~ g e n u n i q u e i d ~ n a m e ~ c o u n t e r ~ = ~
    (fn() => let
            val id = "{"^name^"{"^Int.toString(!counter)^"}}";
            in (counter := !counter + 1; id) end, fn() => (counter := 1))
    val (freshLvr, resetLvr) = genuniqueid "Z_" (ref 1)
val (freshPrg, resetPrg) = genuniqueid "t_" (ref 1)
    val (freshLab, resetLab) = genuniqueid "" (ref 1)
    (* future work, for now, we assume, there are no ex/all rules *)
    fun liftExRule(proof) = proof
    fun liftAllRule(proof) = proof
    5 (* maps the expressions of a proof *)
    fun mapProofExprs f (proof as DT(label, instr, pre, post, rule, antes)) =
        DT(label, instr,
            f pre, f post,
            rule, map (mapProofExprs f) antes)
        | mapProofExprs f (DE(E)) = DE(f E)
    (* maps the sub-expressions in an expression *)
    fun mapSubExprs f expr =
        case expr of
            implies (e1, e2) => implies(f(e1), f(e2))
            | conj (e1, e2) => conj(f(e1), f(e2))
            | allP (v, e) => allP(v, f(e))
            | exP (v, e) => exP(v, f(e))
            | shiftP (e) => shiftP(f(e))
            | unshiftP (e) => unshiftP(f(e))
            | eqP (e1, e2) => eqP(f(e1), f(e2))
            | neP (e1, e2) => neP(f(e1), f(e2))
            | unop (s, e) => unop(s, f(e))
            | binop (s, e1, e2) => binop(s, f(e1), f(e2))
            | replacelvr (s, e1, e2) => replacelvr(s, f(e1), f(e2))
            | replaceprg (s, e1, e2) => replaceprg(s, f(e1), f(e2))
            | replacestk (s, e1, e2) => replacestk(s, f(e1), f(e2))
            | genop (s, L) => genop(s, map f L)
            | e => e
    fun mapExprRec f expr = f(mapSubExprs (mapExprRec f) expr)
o fun mapExprRecCond P f expr =
        if P(expr) then f(mapSubExprs (mapExprRecCond P f) expr)
        else expr
    exception UnshiftOnTOS
    fun doShift e = case e of stk i => stk (i+1) | _ => e
fun doUnshift (stk i) = if i = O then raise UnshiftOnTOS
                                    else stk (i-1)
    | doUnshift e = e
    fun doreplacelvr s By In =
        mapExprRecCond
            (fn(e) => case e of
                        allP (x,_) => s <> x
                | exP (x,_) => s <> x
                | _ => true)
            (fn(e) =>
                case e of lvr x => if s=x then By else e
                        |_=>e)
            In
    fun doreplaceprg s By In =
        mapExprRec
            (fn(e) =>
                case e of prg x => if s=x then By else e
                        |_=>e)
```

```
            In
    fun doreplacestk i By In =
        mapExprRec
        (fn(e) =>
            case e of stk k => if k=i then By else e
                        |_=>e)
            In
180
    fun instrx(P, L, Instr, Q, X) = DT(L, Instr, P, Q, correspondingVMRule Instr, X)
    (** Translation of Expressions **)
    fun CompileX(Qs as conj(Q, PO), e as binop(opname, e1, e2), Qe as conj(Q0, P)) =
        let val Q2 = conj(shiftP(Q), replacestk(0, binop(opname, stk(0), e2), P))
            val Q3 = conj(shiftP(shiftP(Q)),
                                    replacestk(1, binop(opname, stk(1), stk(0)), shiftP(P)))
        in CompileX(Qs, e1, Q2) @ CompileX(Q2, e2, Q3)
            @ [instr(Q3, freshLab(), ibinop opname, Qe)]
        end
    | CompileX(Qs as conj(Q, PO), e as literal c, Qe as conj(Q0, P)) =
        let val L = freshLab()
        in [instr(Qs, L, pushc c, Qe)] end
    | CompileX(Qs as conj(Q, PO), e as prg x, Qe as conj(QO, P)) =
        let val L = freshLab()
        in [instr(Qs, L, pushvar x, Qe)] end
```

```
    fun CompileE(P, e, Q (* shift P ++ e = s(0) *)) =
        let
            val L = freshLab()
            val P' = conj(P, eqP(e,e))
235 in
            [instr(P, L, nop, P')]@ CompileX(P',e,Q)
    end
    fun CompileEPost(P,e) = conj(shiftP(P), eqP(e, stk 0))
    (* Translation of statements *)
240 exception InvalidShape of string;
    (* Compiling the conjunct/disjunct rule, see paper for requirements
        to call this function *)
    fun CompileSConjDisj(proof as DT(_, Comp, P, Q, rule, [pS1, pS2])) = let
        val S1 = CompileS(pS1)
        val S2 = CompileS(pS2)
        fun co(x,y) = case rule of (* abstracting the rule *)
                    xconj => conj(x,y)
                    | xdisj => disj(x,y)
        (* merging two invokevirtual rules *)
        fun mergeivirt([p1 as DT(L, M1, P1, Q1, R1, _)],
                    [p2 as DT(_, M2, P2, Q2, R2, _)]) =
            [DT(L, M1, co(P1,P2), co(Q1,Q2), rule, [p1,p2])]
        (* merging one invokevar with some other rule *)
        fun mergeivar(p1 as DT(L, M1, P1, Q1, rinvokevar, [MA]), p2) =
            DT(L, M1, co(P1,precondition(p2)), co(Q1,postcondition(p2)),
                rule, [merge(MA, p2)])
        (* merging the "common" rules *)
        and merge(p1 as DT(label1, stmt1, pre1, post1, rule1, antes1),
                p2 as DT(label2, stmt2, pre2, post2, rule2, antes2)) =
            case (rule1,rule2) of
                (rinvokevar, _) => mergeivar(p1,p2)
            | (_, rinvokevar) => mergeivar(p2,p1)
            | _ => DT(label1, stmt1, co(pre1,pre2), co(post1,post2),
                rule1, case rule1 of
                    rinvokevirt => mergeivirt(antes1, antes2)
                    | _ => map merge (strictzip antes1 antes2))
    in map merge (strictzip S1 S2) end
    and CompileSInv(proof as DT(_, Comp, P' as conj(P,R), Q', xinv, [pS])) =
    let val S = CompileS(pS)
        fun mergeivirt([(p as DT(L, M, P, Q, _, _))]) =
            [DT(L, M, conj(P,R), conj(Q,R),
                xinv, [p])]
            | mergeivirt ([]) = (print "CompileSInv:empty"; raise InvalidShape("
                CompileSInv:empty"))
            | mergeivirt (X) = (raise InvalidShape("CompileSInv:mergeivirt"))
        fun mergeivar([p as DT(L, M, P, Q, rinvokevar, [pS])]) =
            [DT(L, M, conj(P,R), conj(Q,R),
                rinvokevar, [merge pS])]
        and merge(DT(L, St, pre, post, rule, antes)) =
            DT(L, St, conj(pre,R), conj(post,R), rule,
                case rule of rinvokevirt => mergeivirt antes
                    | rinvokevar => mergeivar antes
                    | _ => map merge antes
                    )
    in map merge S end
285 and CompileSSub(proof as DT(_, Comp, P, as replacelvr(Z,t, P),
                        Q', xsub, [pS])) =
let val S = CompileS(pS)
```

    fun mergeivirt([p as DT(L, M, P, Q, R, _)]) =
    [DT(L, M, replacelvr(Z,t,P), replacelvr(Z,t,Q),
                xsub, [p])]
    fun mergeivar([p as DT(L, M, P, Q, rinvokevar, [pS])]) =
        [DT(L, M, replacelvr(Z,t,P), replacelvr(Z,t,Q),
            rinvokevar, [merge pS])]
    and merge(DT(L, St, pre, post, rule, antes)) =
    DT(L, St, replacelvr(Z,t,pre), replacelvr(Z,t,post), rule,
        case rule of
                        rinvokevar \(=>\) mergeivar antes
                | rinvokevirt \(=>\) mergeivirt antes
                | _ => map merge antes)
    in map merge \(S\) end
    and CompileSInvokeVar
        (proof as DT(_, invoke(x,y,M,e),
                    P, as replacelvr(Z, prg w, P),
                    Qe, xinvokevar, [pI])) =
    let val \(\mathrm{I}=\) CompileS (pI)
        fun merge (p as DT(L, St, pre, post, xinvokevirt, [M]) =
        DT(L, St, replacelvr(Z,prg w, pre), replacelvr(Z,prg w,post),
            rinvokevar, [p])
            | merge(DT(L, St, pre, post, rule, antes)) =
                DT(L, St, replacelvr(Z,prg w,pre), replacelvr(Z,prg w, post), rule,
                    map merge antes)
        in map merge \(I\) end
    and CompileS(DT(_, assign(x, e), PO, P, xassign, [])) =
        let
            val PO' = CompileEPost(PO,e)
    in
        CompileE(PO,e,PO') @ [instr(PO', freshLab(), pop x, P)]
    end
    | CompileS(DT(_, swhile(e, St), P, Q, xwhile, [pS])) =
    let val \(P^{\prime}=\) precondition(pS)
        val E' = CompileEPost(P, e)
        val L = freshLab()
        val \(\mathrm{SB}=\) CompileS (pS)
        val TB = CompileE(P, e, E')
        val \(B=\) freshLab()
        val \(T=\) label(first(TB))
        val \(S\) = label(first(SB))
        in [instr (P, L, goto T, P')] @ SB @ TB @ [instr(E, B, brtrue S, Q)] end
    | CompileS(DT(_, sif(e, St1, St2), P, Q, xif, [pS1, pS2])) =
    let val P1 = precondition (pS1)
        val \(\mathrm{P} 2=\) precondition \((\mathrm{pS} 2)\)
        val \(P^{\prime}=\) CompileEPost (P, e)
            val \(A L=f r e s h L a b()\)
            val \(\mathrm{B} 2=\) CompileS (pS2)
            val BL = freshLab()
            val B1 = CompileS(pS1)
            val EL = freshLab()
            val \(S 1=\) label (first(B1))
            val \(\mathrm{S} 2=\) label(first(B2))
        in CompileE(P,e,P') @ [instr(P', AL, brtrue S1, P2)]
            @ B2 @ [instr (P, BL, goto EL, P1)]
            @ B1 @ [instr (Q, EL, nop, Q)]
        end
        | CompileS(DT(_, seq(St1, St2), P, Q, xseq, [pS1, pS2])) =
        CompileS(pS1) @ CompileS(pS2)
    ```
    | CompileS(proof as DT(_, Comp, P, Q, xconj, [pS1, pS2])) =
        CompileSConjDisj(proof)
    | CompileS(proof as DT(_, Comp, P, Q, xdisj, [pS1, pS2])) =
        CompileSConjDisj(proof)
fun CompileImpl(DT(L, M, P, Q, ximpl, [Body])) =
let val _ = resetLab()
    val S = CompileS(Body)
in DT(L,M,P,Q,rimplementation,
            S@[instr(Q, freshLab(), methodend, Q)]) end
(**** PROOF TRANSFORMATION END ************)
```

Listing 5: Implementation


[^0]:    ${ }^{1}$ Needless to say, what we define here may sometimes differ from the original in [Ben04] and [PHM99]
    ${ }^{2}$ e.g. are all variables definitely assigned when they are used, do the values on the stack contain values of the right type for the operations applied to them, are all statements reachable?

[^1]:    ${ }^{3}$ remember that we are considering individual methods only

[^2]:    ${ }^{4}$ method identifiers of the form $T: m$ or $T @ m$

[^3]:    ${ }^{5}$ in a very general sense
    ${ }^{6}$ [JAR03] gives an overview, [SS03] discusses the problems of bytecode verification

[^4]:    ${ }^{7}$ which means: $\mathfrak{p} ;\langle S, \sigma, l\rangle \rightarrow\left\langle S^{\prime}, \sigma^{\prime}, l^{\prime}\right\rangle$
    $8^{s}$ see the definition of the invokevirtual rule

[^5]:    ${ }^{9}$ there are some minor limitations given in section 4.5 on page 26

[^6]:    ${ }^{11}$ invokevirtual is a special case: easy generalization would allow both rules to be combined into one, but it is technically convenient to keep them separate

[^7]:    ${ }^{12}$ we have to "merge" one invocation-var rule with another rule (either a invocation-var or a invokevirtual rule)

[^8]:    ${ }^{13}$ we called it $f, g$-form

